

# **MODULE MANUAL**

**BACHELOR'S DEGREE**

## **Electronic Engineering**

**DEGREE: BACHELOR OF ENGINEERING**

**Validity period: September 1<sup>st</sup> 2024 to August 31<sup>st</sup> 2025**

**According to examination regulations from 2024**

## Content

|   |    |
|---|----|
| Overview .....                            | 4  |
| Examination and Course Achievements ..... | 6  |
| Excursions .....                          | 6  |
| Bonus points .....                        | 6  |
| Weighting .....                           | 6  |
| Compulsory Modules .....                  | 7  |
| Electronic Engineering 1 .....            | 8  |
| Programming 1 .....                       | 10 |
| Computer Engineering .....                | 12 |
| Physical Foundations .....                | 14 |
| Mathematics 1 .....                       | 16 |
| Scientific Work .....                     | 18 |
| Electronic Engineering 2 .....            | 20 |
| Programming 2 .....                       | 22 |
| Computer Networks .....                   | 24 |
| Engineering Design .....                  | 26 |
| Mathematics 2 .....                       | 28 |
| Audio and Video Technologies .....        | 30 |
| Microelectronics .....                    | 32 |
| Software Engineering .....                | 34 |
| Embedded Systems .....                    | 36 |
| Mathematics 3 .....                       | 38 |
| Audio and Video Processing .....          | 39 |
| Control Engineering 1 .....               | 41 |
| Hardware Engineering 1 .....              | 43 |
| Prototyping and Systems Engineering ..... | 45 |
| Mathematics 4 .....                       | 48 |
| Business Communication .....              | 50 |
| Internship / Exchange Semester .....      | 52 |
| Control Engineering 2 .....               | 54 |
| Hardware Engineering 2 .....              | 56 |
| Project Work .....                        | 58 |
| Bachelor Thesis incl. Colloquium .....    | 60 |
| Special Emphasis A .....                  | 62 |
| Autonomous Systems A .....                | 63 |

|   |    |
|---|----|
| Embedded Electronic Engineering A ..... | 66 |
| Special Emphasis B .....                | 69 |
| Autonomous Systems B.....               | 70 |
| Embedded Electronic Engineering B ..... | 74 |

## Overview

**Legend:** ECTS - European Credit Transfer and Accumulation System, HPW – Hours Per Week, WL – Workload, CH – Contact Hours, SST – Self-Study Time

| Module Name                  | Module Abbreviation | Module Coordinator                        | ECTS Credits | HPW | WL  | CH | SST |
|------------------------------|---------------------|---|--------------|-----|-----|----|-----|
| Electronic Engineering 1     | ELE-B-2-1.01        | Prof. Dr.-Ing. João Paulo Javidi da Costa | 5            | 4   | 150 | 60 | 90  |
| Programming 1                | ELE-B-2-1.02        | Prof. Dr. Georg Birkenheuer               | 5            | 4   | 150 | 60 | 90  |
| Computer Engineering         | ELE-B-2-1.03        | Prof. Dr. Stefan Henkler                  | 5            | 4   | 150 | 60 | 90  |
| Physical Foundations         | ELE-B-2-1.04        | Prof. Dr. Emanuel Slaby                   | 5            | 4   | 150 | 60 | 90  |
| Mathematics 1                | ELE-B-2-1.05        | Prof. Dr. Jan Eric Kyprianidis            | 5            | 4   | 150 | 60 | 90  |
| Scientific Work              | ELE-B-2-1.06        | Dr. Birte Horn                            | 5            | 4   | 150 | 60 | 90  |
| Electronic Engineering 2     | ELE-B-2-2.01        | Prof. Dr. Jan Eric Kyprianidis            | 5            | 4   | 150 | 90 | 60  |
| Programming 2                | ELE-B-2-2.02        | Prof. Dr. Stefan Henkler                  | 5            | 4   | 150 | 60 | 90  |
| Computer Networks            | ELE-B-2-2.03        | Prof. Dr. Emanuel Slaby                   | 5            | 4   | 150 | 60 | 90  |
| Engineering Design           | ELE-B-2-2.04        | Prof. Dr. Emanuel Slaby                   | 5            | 4   | 150 | 60 | 90  |
| Mathematics 2                | ELE-B-2-2.05        | Prof. Dr. Jan Eric Kyprianidis            | 5            | 4   | 150 | 60 | 90  |
| Audio and Video Technologies | ELE-B-2-2.06        | Prof. Stefan Albertz                      | 5            | 3   | 150 | 45 | 105 |
| Microelectronics             | ELE-B-2-3.01        | Prof. Dr.-Ing. Ali Hayek                  | 5            | 4   | 150 | 60 | 90  |
| Software Engineering         | ELE-B-2-3.02        | Prof. Dr. Stefan Henkler                  | 5            | 4   | 150 | 60 | 90  |
| Embedded Systems             | ELE-B-2-3.03        | Prof. Dr. Achim Rettberg                  | 10           | 6   | 300 | 90 | 210 |
| Mathematics 3                | ELE-B-2-3.04        | Prof. Dr. Jan Eric Kyprianidis            | 5            | 4   | 150 | 60 | 90  |

| Module Name                         | Module Abbreviation | Module Coordinator                        | ECTS Credits | HPW | WL  | CH  | SST |
|-------------------------------------|---------------------|---|--------------|-----|-----|-----|-----|
| Audio and Video Processing          | ELE-B-2-3.05        | Prof. Stefan Albertz                      | 5            | 3   | 150 | 45  | 105 |
| Control Engineering 1               | ELE-B-2-4.01        | Prof. Dr.-Ing. João Paulo Javidi da Costa | 5            | 4   | 150 | 60  | 90  |
| Hardware Engineering 1              | ELE-B-2-4.02        | Prof. Dr.-Ing. Ali Hayek                  | 5            | 4   | 150 | 60  | 90  |
| Prototyping and Systems Engineering | ELE-B-2-4.03        | Prof. Dr. Stefan Henkler                  | 10           | 6   | 300 | 90  | 210 |
| Mathematics 4                       | ELE-B-2-4.04        | Prof. Dr. Jan Eric Kyprianidis            | 5            | 4   | 150 | 60  | 90  |
| Business Communication              | ELE-B-2-4.05        | Dr. Birte Horn                            | 5            | 3   | 150 | 45  | 105 |
| Internship / Exchange Semester      | ELE-B-2-5.01        | Prof. Dr. Stefan Henkler                  | 30           | -   | 900 | 10  | 890 |
| Control Engineering 2               | ELE-B-2-6.01        | Prof. Dr.-Ing. João Paulo Javidi da Costa | 5            | 4   | 150 | 60  | 90  |
| Hardware Engineering 2              | ELE-B-2-6.02        | Prof. Dr.-Ing. Ali Hayek                  | 5            | 4   | 150 | 60  | 90  |
| Project Work                        | ELE-B-2-6.03        | Prof. Dr. Achim Rettberg                  | 8            | -   | 240 | -   | 240 |
| Autonomous Systems A                | ELE-B-2-6.04        | Prof. Dr. Stefan Henkler                  | 12           | 8   | 360 | 120 | 240 |
| Embedded Electronic Engineering A   | ELE-B-2-6.05        | Prof. Dr. Achim Rettberg                  | 12           | 8   | 360 | 120 | 240 |
| Bachelor Thesis incl. Colloquium    | ELE-B-2-7.01        | Prof. Dr. Stefan Henkler                  | 15           | -   | 450 | -   | 450 |
| Autonomous Systems B                | ELE-B-2-7.02        | Prof. Dr. Stefan Henkler                  | 15           | 10  | 450 | 150 | 300 |
| Embedded Electronic Engineering B   | ELE-B-2-7.03        | Prof. Dr.-Ing. Ali Hayek                  | 15           | 10  | 450 | 150 | 300 |

## **Examination and Course Achievements**

For the final pass of the modules, the exams in the module description named examination have to be provided. These are assessed and used to determine the module grade. The examination forms are regulated in the framework examination regulations (RPO) for the bachelor's degree programs at Hamm-Lippstadt University of Applied Sciences.

For the passing of a certain module, different course achievements are to be included. Course achievements are not graded and are therefore not included in the module grade. Examination achievements can be taken without a successful achievement of the course achievement(s). A module is only considered completed when all examinations and all course achievements have been successfully completed. The module descriptions in this module manual deal with the course achievements that are part of the framework examination regulations (RPO) for the bachelor's degree programs at Hamm-Lippstadt University of Applied Sciences.

## **Excursions**

Excursions (to companies, trade shows, museums, exhibitions, conferences, events, etc.) may be included as part of a course/module to enhance students' understanding of the subject matter.

## **Bonus points**

Bonus points may be awarded. Details will be announced at the beginning of lectures.

## **Weighting**

The weighting will be based on the SWS ratio of the courses in the module if a module exam consists of more than one exam. Otherwise, an explicit weighting is given.

## **Compulsory Modules**

|                            |   |
|----------------------------|---|
| <b>Module Name</b>         | <b>Electronic Engineering 1</b>           |
| <b>Module Abbreviation</b> | ELE-B-2-1.01                              |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. João Paulo Javidi da Costa |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|  |   |
|--|---|
| <b>Qualification Objectives</b>                    | The students acquire skills in the fundamentals of DC technology and transients. They can calculate and analyze direct current networks. They can calculate capacitances and inductances and calculate and analyze 1st and 2nd order transients.  |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Voltage, Current, and Power</li> <li>• Resistance and Ohm's law</li> <li>• Kirchhoff's laws</li> <li>• Node voltage and mesh current analysis</li> <li>• Thévenin- and Norton equivalent</li> <li>• Capacitance and inductance</li> <li>• Transients of first- and second-order</li> </ul> |
| <b>Teaching Method</b>                             | Lecture (2 HPW), Exercise (2 HPW)   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises, relevant tasks will be completed, and the results will be discussed. Moreover, experiments related to electric circuits will be conducted.  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography/Literature</b>                     | <ul style="list-style-type: none"> <li>• R. Hambley, Electrical Engineering: Principles and Applications, 6th ed. Pearson Education, 2014.</li> </ul>   |



|  |  |
|--|--|
|  | <ul style="list-style-type: none"><li>• J. W. Nilsson and S. A. Riedel, "Electric Circuits," Pearson Publisher, 11th Ed., 2018</li><li>• J. D. Irwin and R. M. Nelms, "Basic Engineering Circuit Analysis," Wiley Publisher, 11th Ed., 2015</li><li>• J. A. Svoboda and R. C. Dorf, "Introduction to Electric Circuits," Wiley Publisher, 9th Ed., 2013</li><li>• K. Alexander and M. N. O. Sadiku, "Fundamentals of Electric Circuits," McGraw Hill Education Publisher, 6h Ed., 2019</li></ul> |
|--|--|

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Programming 1</b>        |
| <b>Module Abbreviation</b> | ELE-B-2-1.02                |
| <b>Module Coordinator</b>  | Prof. Dr. Georg Birkenheuer |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | <p>Students acquire the competencies necessary to implement software from a professional point of view:</p> <ul style="list-style-type: none"> <li>• Students understand basic terms, methods and concepts of programming and apply them.</li> <li>• They will be able to use programming languages (C and C++) and apply them.</li> <li>• They can analyze problems from the practice of programming by applying the methods of computer science.</li> <li>• Practical problems can be solved independently in the taught programming language, by applying the basics of (object-oriented) programming.</li> </ul> <p>The theoretical and practical work in the fields of programming and software engineering make up the basis that will allow students to master and design software-intensive systems. These include, for example, autonomous systems, which are the subject matter of other courses such as Microcontrollers and Advanced Embedded Systems.</p> |
| <b>Content</b>                  | <p>Basics of programming languages</p> <ul style="list-style-type: none"> <li>• Variables, strings</li> <li>• Data types and operators</li> <li>• Logic</li> <li>• Branching and repetitions</li> <li>• Functions, methods and recursion</li> <li>• Arrays</li> </ul> <p>Basic object-oriented programming concepts</p> <ul style="list-style-type: none"> <li>• Classes and objects</li> <li>• Attributes and methods</li> <li>• Encapsulation</li> <li>• Inheritance and polymorphism</li> <li>• Exception handling</li> <li>• Abstract data types (wrappers, lists, trees,</li> </ul>   |

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• dictionaries, queues, cellars and enumerations)</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 HPW), Exercise (2 HPW)  |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography/Literature</b>                     | <ul style="list-style-type: none"> <li>• B. W. Kernighan and D. M. Ritchie, The C programming language. Englewood Cliffs, N.J.: Prentice Hall, 1988.</li> <li>• B. Stroustrup and an O. M. C. Safari, A Tour of C++, Second Edition. 2018.</li> <li>• D. Griffiths and D. Griffiths, Head first C. Beijing: O'Reilly Media, 2012.</li> <li>• P. Prinz and T. Crawford, C in a nutshell: the definitive reference, Second edition. Sebastopol, CA: O'Reilly Media, Inc., 2015.</li> <li>• P. Barry and D. Griffiths, Head first programming: [a learner's guide to programming using the Python language]. Sebastopol, CA: O'Reilly, 2009.</li> </ul> |

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Computer Engineering</b> |
| <b>Module Abbreviation</b> | ELE-B-2-1.03                |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler    |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|   |   |
|---|---|
| <b>Qualification Objectives</b>               | <p>The students will acquire competencies in the basics of Computer Engineering:</p> <ul style="list-style-type: none"> <li>• The students will know the basic terms, methods and concepts of computer science.</li> <li>• They will know and be able to explain how a computer is built.</li> <li>• They will know the basic elements of switching algebra and will be able to independently create and explain simple circuits based on a description of the problem.</li> <li>• The students will know how an operating system works and will be able to explain it.</li> </ul> <p>The theoretical and practical work will be a foundation for the analyzing of embedded systems and microcontrollers.</p> |
| <b>Content</b>                                | <p>Fundamentals of Computer Architecture</p> <ul style="list-style-type: none"> <li>• Processors</li> <li>• Memory</li> <li>• Interfaces</li> </ul> <p>Fundamentals of System Software</p> <ul style="list-style-type: none"> <li>• Memory Management</li> <li>• Resource Management</li> <li>• Processes</li> </ul>  |
| <b>Teaching Method</b>                        | Lecture (2 HPW), Exercise (2 HPW)   |
| <b>Course / Teaching and Learning Methods</b> | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.  |
| <b>Examination Forms</b>                      | Module exam as a written exam (90 minutes).   |

|  |   |
|--|---|
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"><li>• Tanenbaum, Andrew S. Structured Computer Organization. Pearson, 6th Edition, 2013.</li><li>• Tanenbaum, Andrew S. Operating Systems Design and Implementation, Pearson, 3rd Edition, 2006.</li><li>• Tanenbaum, Andrew S. Modern Operating Systems, Pearson, 3rd Edition, 2007.</li><li>• B. W. Kernighan and D. M. Ritchie, The C programming language. Englewood Cliffs, N.J.: Prentice Hall, 1988.</li></ul> |

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Physical Foundations</b> |
| <b>Module Abbreviation</b> | ELE-B-2-1.04                |
| <b>Module Coordinator</b>  | Prof. Dr. Emanuel Slaby     |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | The students will know the basic physical phenomena, which will be needed in the continuing natural and engineering disciplines. The students will possess the competence to solve physical tasks in an engineering context and to apply the basic laws of physics. The competencies learned represent the basics of prototype development.  |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Fundamentals of Mechanics:           <ul style="list-style-type: none"> <li>○ Kinematics and Dynamics</li> </ul> </li> <li>• The Laws of Thermodynamics</li> <li>• Light and Optical Systems           <ul style="list-style-type: none"> <li>○ Geometric Optics, Wave Optics and Surface Phenomena</li> </ul> </li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw)  |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. The teaching units will build on one another and will be based on the students' experiences. In addition, the students will complete tasks during the contact hours under supervision of the instructor. Care will be taken to ensure that every student is involved and obvious knowledge gaps will be immediately dealt with through in-depth explanation. |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed module final exam.  |
| <b>Use of the module (in other study programs)</b> | -  |

|                                  |  |
|----------------------------------|--|
| <b>Bibliography / Literature</b> | <ul style="list-style-type: none"><li>• R. Shankar: “Fundamentals of Physics: Mechanics, Relativity, and Thermodynamics”, Open Yale Courses 2014, ISBN-10: 0300192207.</li><li>• R. Shankar: “Fundamentals of Physics: Electromagnetism, Optics, and Quantum Mechanics”, Open Yale Courses 2014, ISBN-10: 0300212364”.</li><li>• M. Alonso, E.J. Finn: “Physics”, Addison Wesley Pub Co Inc. 1992, ISBN-10: 0201565188.</li><li>• D. Halliday, R. Resnick, J. Walker: “Fundamentals of Physics”, Wiley 2010, ISBN-10: 0470469080.</li><li>• D. C. Giancoli: “Physics: Principles with Applications”, Pearson 2013, ISBN-10: 0321625927.</li><li>• The Feynman Lectures on Physics, “Vol. I: The New Millennium Edition: Mainly Mechanics, Radiation, and Heat”. ISBN-10: 0465024939.</li></ul> |
|----------------------------------|--|

|                            |                                |
|----------------------------|--------------------------------|
| <b>Module Name</b>         | <b>Mathematics 1</b>           |
| <b>Module Abbreviation</b> | ELE-B-2-1.05                   |
| <b>Module Coordinator</b>  | Prof. Dr. Jan Eric Kyprianidis |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|   |   |
|---|---|
| <b>Qualification Objectives</b>                   | Students acquire skills in basic mathematical concepts and procedures. They can explain and apply basic concepts of logic and set theory. They can calculate with real and complex numbers and solve equations, inequalities and linear systems of equations. They can analyze sequences for convergence and functions for continuity. They can derive and integrate functions and solve 1st order differential equations. For typical tasks in the field of technical systems they can select and apply the appropriate learned methods and interpret the results. |
| <b>Content</b>                                    | <ul style="list-style-type: none"> <li>• Logic, set theory, functions</li> <li>• Real and complex numbers, fractions, powers, roots, logarithms, trigonometry, equations and inequalities</li> <li>• Systems of linear equations</li> <li>• Sequences and limits</li> <li>• Differential calculus</li> <li>• Integral calculus</li> <li>• First-order differential equations</li> </ul>   |
| <b>Teaching Method</b>                            | Lecture (2 hpw), Exercise (2 hpw)   |
| <b>Course / Teaching and Learning Methods</b>     | The lecture is held in seminar style. In the exercises, practice problems are worked on and the results of practice problems are discussed.   |
| <b>Examination Forms</b>                          | Module exam as written exam (90 minutes).   |
| <b>Participation Recommendations</b>              | None.   |
| <b>Prerequisite for the award of ECTS credits</b> | Passed module final exam.   |



|  |  |
|--|--|
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A. Croft and R. Davison, Foundation Maths, 6th ed. Pearson Education, 2016.</li> <li>• A. Croft and R. Davison, Mathematics for Engineers, 5th ed. Pearson Education, 2017.</li> <li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li> </ul> |

|                            |                        |
|----------------------------|------------------------|
| <b>Module Name</b>         | <b>Scientific Work</b> |
| <b>Module Abbreviation</b> | ELE-B-2-1.06           |
| <b>Module Coordinator</b>  | Dr. Birte Horn         |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 1st Semester / Winter Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p>Students will be able to communicate adequately in the English language during their studies and future career by purposefully using methods and techniques of scientific communication:</p> <ul style="list-style-type: none"> <li>• They will understand how to communicate and correspond appropriately both verbally and in writing.</li> <li>• They will have the necessary knowledge to understand scientific and technical texts in English and to write English texts themselves.</li> <li>• They will have basic knowledge of scientific work that enables them to carry out project work, presentations and dissertations in a structured, scientifically correct and legally secure manner.</li> <li>• Furthermore, the students will acquire intercultural competencies, which they can apply to various communication scenarios.</li> </ul> <p>The students will have theoretical knowledge and practical methods for effective and efficient learning and working and will know models, strategies and techniques in the field of self-management. They will be encouraged to take new courses of action in a goal-oriented manner and to use methods to expand their self-management skills in the professional, academic and private sectors and to be able to constantly act in a more successful manner.</p> |
| <b>Content</b>                  | <ul style="list-style-type: none"> <li>• Subject-specific development of language skills</li> <li>• Refreshing and strengthening grammatical knowledge</li> <li>• Editing and writing scientific and technical texts and articles</li> <li>• Technical conversation and communication</li> <li>• Presentations and lectures</li> <li>• Scientific work</li> <li>• Topic choice</li> <li>• Putting the research question and procedure into concrete terms</li> <li>• Research and evaluation</li> <li>• Conducting own scientific examinations</li> </ul>   |

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• Structuring and organizing the contents</li> <li>• Scientific writing style</li> <li>• Source citation, copyright and plagiarism</li> <li>• Reflection and follow-up of conversations</li> <li>• Presentation</li> <li>• Visualization of presentations</li> <li>• Time and stress management</li> <li>• Self-reflection</li> </ul>   |
| <b>Teaching Method</b>                             | Seminar (2 hpw), Exercise (2 hpw)  |
| <b>Course / Teaching and Learning Methods</b>      | Individual and group work, presentations, reflection and feedback discussion and role playing to develop intercultural competencies. A professional excursion may be conducted as part of the course   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes, 60%) and in-class presentation (15 minutes, 40%) or term paper (7-10 pages, 60%) and in-class presentation (15 minutes, 40%).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed module final exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• Allen, David: Getting things done. The art of stress-free productivity. New York: Penguin, 2003.</li> <li>• Brieger, Nick; Pohl, Alison: Technical English Vocabulary and Grammar. Munich: Langenscheidt, 2004.</li> <li>• Chastain, Emma. How to write a research paper. New York: Barnes &amp; Nobles Publ., 2006.</li> <li>• Crowley, Dermot. Smart work. Centralise, organize, realise. How to boost your productivity in 3 easy steps. John Wiley&amp; Sons, 2016.</li> <li>• Maslow, A.H. Motivation and personality. New York: Harper, 1954.</li> <li>• Maier, Rolf and Engelmeyer, Eva. Zeitmanagement: Grundlagen, Methoden und Techniken. Offenbach: Gabal, 2004.</li> <li>• Rehborn, Angelika. Brückenkurs Wissenschaftliches Arbeiten. Konstanz/München: UKV Lucius, 2015.</li> <li>• Skern, Tim. Writing Scientific English. Wien: UTB, 2009.</li> </ul> |

|                            |                                 |
|----------------------------|---------------------------------|
| <b>Module Name</b>         | <b>Electronic Engineering 2</b> |
| <b>Module Abbreviation</b> | ELE-B-2-2.01                    |
| <b>Module Coordinator</b>  | Prof. Dr. Jan Eric Kyprianidis  |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 90  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 60  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|  |   |
|--|---|
| <b>Qualification Objectives</b>                    | Students acquire skills in the fundamentals of alternating current technology. They are able to calculate and analyze AC circuits using phasors and complex impedances. They can describe filters by means of the transfer function and, for simple first-order filters, calculate the transfer function and create Bode plots. They can calculate the parameters of series and parallel resonant circuits. They can analyze and calculate amplifiers in simple circuits. |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Sinusoidal signals</li> <li>• Phasors and impedances</li> <li>• Circuit analysis with phasors and impedances</li> <li>• Power in AC circuits</li> <li>• Filter, transfer function, and Bode plots</li> <li>• First-order low-pass and high-pass filters</li> <li>• Series and parallel resonance</li> <li>• Second-order filters</li> <li>• Amplifiers</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw)   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |

|                                  |  |
|----------------------------------|--|
| <b>Bibliography / Literature</b> | <ul style="list-style-type: none"><li>• R. Hambley, Electrical Engineering: Principles and Applications, 6th ed. Pearson Education, 2014.</li><li>• J. W. Nilsson and S. A. Riedel, "Electric Circuits," Pearson Publisher, 11th Ed., 2018</li><li>• J. D. Irwin and R. M. Nelms, "Basic Engineering Circuit Analysis," Wiley Publisher, 11th Ed., 2015</li><li>• J. A. Svoboda and R. C. Dorf, "Introduction to Electric Circuits," Wiley Publisher, 9th Ed., 2013</li><li>• K. Alexander and M. N. O. Sadiku, "Fundamentals of Electric Circuits," McGraw Hill Education Publisher, 6h Ed., 2019</li></ul> |
|----------------------------------|--|

|                            |                          |
|----------------------------|--------------------------|
| <b>Module Name</b>         | <b>Programming 2</b>     |
| <b>Module Abbreviation</b> | ELE-B-2-2.02             |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|   |  |
|---|--|
| <b>Qualification Objectives</b>               | <p>The students are able to implement software from a professional point of view</p> <ul style="list-style-type: none"> <li>• students are able to apply advanced terms, methods and concepts of programming</li> <li>• they can analyze problems from the practice of programming,</li> <li>• more complex practical problems can be solved independently in the taught programming language (C++) by applying the basics of object-oriented programming.</li> </ul>  |
| <b>Content</b>                                | <p>Threads, concurrent and distributed programs</p> <ul style="list-style-type: none"> <li>• Basics of concurrent programs</li> <li>• Thread properties and states</li> <li>• Synchronization concepts</li> <li>• Network programming / distributed programming</li> </ul> <p>Data streams and serialization</p> <ul style="list-style-type: none"> <li>• Storing persistent objects / objects</li> <li>• Files, directories and file accesses</li> </ul> <p>Introduction to Graphical Programming</p> <ul style="list-style-type: none"> <li>• GUI and event processing</li> <li>• Simple drawing methods</li> <li>• Strings</li> <li>• Geometric objects</li> </ul> <p>Advanced topics</p> <ul style="list-style-type: none"> <li>• e.g. generics, current libraries, comparisons with other OO programming languages, etc.</li> </ul> |
| <b>Teaching Method</b>                        | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b> | The lecture is held in seminar style. In the exercises, the results of tasks are discussed, exercises are worked on or small projects are carried out.   |

|  |  |
|--|--|
|  | To deepen the lectures, excursions are possible (companies, fairs, museums, exhibitions, congresses, events, etc.).  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• B. W. Kernighan and D. M. Ritchie, The C programming language. Englewood Cliffs, N.J.: Prentice Hall, 1988.</li> <li>• B. Stroustrup and an O. M. C. Safari, A Tour of C++, Second Edition. 2018.</li> <li>• D. Griffiths and D. Griffiths, Head first C. Beijing: O'Reilly Media, 2012.</li> <li>• P. Prinz and T. Crawford, C in a nutshell: the definitive reference, Second edition. Sebastopol, CA: O'Reilly Media, Inc., 2015.</li> <li>• P. Barry and D. Griffiths, Head first programming: [a learner's guide to programming using the Python language]. Sebastopol, CA: O'Reilly, 2009.</li> </ul> |

|                            |                          |
|----------------------------|--------------------------|
| <b>Module Name</b>         | <b>Computer Networks</b> |
| <b>Module Abbreviation</b> | ELE-B-2-2.03             |
| <b>Module Coordinator</b>  | Prof. Dr. Emanuel Slaby  |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|   |  |
|---|--|
| <b>Qualification Objectives</b>                   | Students will be able to analyze the operation of computer networks by understanding the basic elements, network elements, network technologies and protocols and identifying, naming and evaluating them. They are able to design their own networks.   |
| <b>Content</b>                                    | <p>Computer Networks</p> <ul style="list-style-type: none"> <li>• Technical Basics           <ul style="list-style-type: none"> <li>○ Bit transmission</li> <li>○ Methods for message transmission</li> </ul> </li> <li>• ISO/OSI reference model</li> <li>• Ethernet</li> <li>• Basic protocols in IP-based networks           <ul style="list-style-type: none"> <li>○ IPv4 and IPv6</li> <li>○ TCP and UDP</li> <li>○ ARP, ICMP, DHCP and DNS</li> </ul> </li> <li>• Internet application protocols           <ul style="list-style-type: none"> <li>○ HTTP, FTP and e-mail</li> </ul> </li> <li>• Virtual networks</li> <li>• Security in computer networks</li> </ul> |
| <b>Teaching Method</b>                            | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b>     | The lectures are held in seminar style. In the exercises, tasks are worked on and the results are discussed. For a large part of the exercises, the network simulation tool FILIUS is used to create and analyze networks. The exercises take place in a PC pool equipped with the necessary software.   |
| <b>Examination Forms</b>                          | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>              | None.  |
| <b>Prerequisite for the award of ECTS credits</b> | Passed final module exam.  |



|  |  |
|--|--|
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"><li>• S. Tanenbaum and D. Wetherall, <i>Computer networks</i>, 5th ed. Boston: Pearson Prentice Hall, 2011.</li><li>• C.-H. Wu, <i>Introduction to computer networks and cybersecurity</i>. Boca Raton: CRC Press/Taylor &amp; Francis Group, 2013.</li><li>• L. L. Peterson and B. S. Davie, <i>Computer networks: a systems approach</i>, 5th ed. Amsterdam ; Boston: Morgan Kaufmann, 2012.</li></ul> |

|                            |                           |
|----------------------------|---------------------------|
| <b>Module Name</b>         | <b>Engineering Design</b> |
| <b>Module Abbreviation</b> | ELE-B-2-2.04              |
| <b>Module Coordinator</b>  | Prof. Dr. Emanuel Slaby   |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|   |   |
|---|---|
| <b>Qualification Objectives</b>               | <p>The students will acquire competencies in the basics of engineering design</p> <ul style="list-style-type: none"> <li>• They will know the basic terms of engineering design and be able to explain them.</li> <li>• They will know the basic techniques of engineering design and be able to apply them.</li> <li>• Students will be familiar with the many possibilities that arise through design using CAD and can apply basic functions by using the techniques of design theory.</li> </ul> <p>The practical and theoretical skills learned will serve as the basis for prototyping.</p> |
| <b>Content</b>                                | <ul style="list-style-type: none"> <li>• Construction processes</li> <li>• Definition of tasks, requirements and functions</li> <li>• Creativity techniques</li> <li>• Basic rules of design</li> <li>• Basics Human Centered Design</li> <li>• Drawing principles</li> <li>• Illustrations, sections, dimensioning</li> <li>• Tolerances, fits and surfaces</li> <li>• Materials and production methods</li> <li>• Early prototyping</li> </ul>  |
| <b>Teaching Method</b>                        | Lecture (1 hpw), Exercise (1 hpw), Practise (2hpw).   |
| <b>Course / Teaching and Learning Methods</b> | <p>Course content in the lecture is taught using slides or pictures on the board. The applicability of the content in practice will be examined and will be explained by examples. In the exercises, lecture content will be more deeply examined by means of appropriate exercises. In doing so, the students will have the opportunity to complete the exercises on the board under moderation of the lecturer. Questions that the students may have will be discussed and answered in groups.</p>  |

|  |   |
|--|---|
|  | <p>Excursions are also possible.</p> <p>In the lab work, the educational content is partially taught based on slides or pictures on the board in the context. The course will be held in the PC lab rooms. The CAD software SolidWorks will be presented in a practical manner and the students will learn how to use it based on design examples.</p>  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).  |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• Dieter, George, Schmid, Linda: Engineering Design, McGraw-Hill Education, 5th edition, 2012, ISBN-10: 0073398144.</li> <li>• Pahl, Gerhard; Beitz, W.: Engineering Design: A Systematic Approach, Springer; 3rd edition, 2007, ISBN-10: 1846283183.</li> <li>• Norman, Don: The Design of Everyday Things: Revised and Expanded Edition, Basic Books, 2nd edition, 2013, ISBN-13: 978-0465050659</li> <li>• Howard, William; Musto, Joseph: Introduction to Solid Modeling Using SolidWorks, McGraw-Hill Science/Engineering/Math, 9th edition, 2013, ISBN-10: 0073522694.</li> <li>• Lefteri, Chris: Making it: manufacturing techniques for product design, Laurence King Pub, 2nd edition, 2012, ISBN-13: 978-1856697491.</li> <li>• Warnier, Verbruggen, Unfold (eds.): Printing Things: Visions and Essentials for 3D Printing, Die Gestalten Verlag, 1st edition, 2014, ISBN-13: 978-3899555165</li> </ul> |

|                            |                                |
|----------------------------|--------------------------------|
| <b>Module Name</b>         | <b>Mathematics 2</b>           |
| <b>Module Abbreviation</b> | ELE-B-2-2.05                   |
| <b>Module Coordinator</b>  | Prof. Dr. Jan Eric Kyprianidis |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | Students acquire further skills in basic mathematical concepts and procedures. They can calculate with vectors and matrices. They can develop functions into their real and complex Fourier series and solve linear differential equations. For typical tasks in the field of technical systems, they can select and apply the appropriate methods learned and interpret the results.                            |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Vectors and matrices</li> <li>• Fourier series</li> <li>• Linear differential equations</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A. Croft and R. Davison, Foundation Maths, 6th ed. Pearson Education, 2016.</li> <li>• A. Croft et al., Engineering Mathematics, 5th ed. Pearson Education, 2017.</li> <li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li> <li>• G. James et al., Advanced Modern Engineering Mathematics, 4th ed. Prentice Hall, 2011.</li> </ul> |

|  |   |
|--|---|
|  | <ul style="list-style-type: none"><li>• D.G. Zill and W.S. Wright, Advanced Engineering Mathematics, 5th ed. Jones &amp; Bartlett Learning, 2014.</li></ul> |
|--|---|

|                            |                                     |
|----------------------------|-------------------------------------|
| <b>Module Name</b>         | <b>Audio and Video Technologies</b> |
| <b>Module Abbreviation</b> | ELE-B-2-2.06                        |
| <b>Module Coordinator</b>  | Prof. Stefan Albertz                |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 3       | <b>Contact Hours</b>   | 45  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 105 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 2nd Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | Students will understand the classical audiovisual media technologies and their methods of image capturing, imaging and audio reproduction. They will be able to judge existing technologies and qualitatively analyze and apply new ones in order to use signal processing for prototype design.  |
| <b>Content</b>                  | <p>Digital Imaging Technology</p> <ul style="list-style-type: none"> <li>• Raster Graphics</li> <li>• Resolutions</li> <li>• Formats</li> <li>• Standards Color</li> <li>• Depth</li> </ul> <p>Image Processing</p> <ul style="list-style-type: none"> <li>• Color Channels</li> <li>• Quantization</li> <li>• Dithering</li> <li>• Normalization</li> </ul> <p>Compositing</p> <ul style="list-style-type: none"> <li>• Mattes and Masks</li> <li>• Procedural Mask Generation</li> <li>• Pattern Tracking and Stabilization</li> <li>• Basic Compositing Processes</li> </ul> <p>A/V Media</p> <ul style="list-style-type: none"> <li>• Media Formats</li> <li>• Codecs</li> <li>• Containers</li> <li>• Distribution and Areas of Application</li> <li>• Digital Cameras</li> </ul> <p>A/V Measurement Procedures</p> <ul style="list-style-type: none"> <li>• Waveform Monitors</li> <li>• Vectorscopes</li> </ul> <p>Image Reproduction Methods</p> |

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• Display Technologies Basics</li> </ul> Image Compression <ul style="list-style-type: none"> <li>• Fundamentals</li> <li>• Chroma Subsampling</li> <li>• JPEG Methods</li> <li>• Discrete Cosine Transformation</li> </ul>   |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (1 hpw).   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• Brinkmann, R. (2008): The Art and Science of Digital Compositing, Morgan Kaufmann, Elsevier Ltd., Oxford, ISBN 978-0123706386.</li> <li>• Poynton, C. A. (2012): Digital Video and HD: Algorithms and Interfaces, Morgan Kaufmann, ASIN B00Y2QVVLA.</li> <li>• Rickitt, R. (2006): Special Effects: The History and Technique, Aurum Press, ISBN 978-1845131302.</li> </ul> |

|                            |                          |
|----------------------------|--------------------------|
| <b>Module Name</b>         | <b>Microelectronics</b>  |
| <b>Module Abbreviation</b> | ELE-B-2-3.01             |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. Ali Hayek |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 3rd Semester / Summer Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | Students acquire skills in the fundamentals of semiconductor devices. They can analyze and calculate diodes, field effect transistors, bipolar transistors and operational amplifiers in simple circuits. They can explain basic properties of logic gates and the circuit techniques for their implementation.  |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Diodes</li> <li>• Bipolar Transistors</li> <li>• Field Effect Transistors</li> <li>• CMOS Technology</li> <li>• Operational amplifier</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw)  |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• R. Hambley, Electrical Engineering: Principles and Applications, 6th Edition, Pearson Education, 2014.</li> <li>• A. S. Sedra, K. C. Smith, T. C. Carusone and V. Gaudet, Microelectronic Circuits. 8th Edition, 2019</li> <li>• D. A. Neamen, Semiconductor Physics and Devices: Basic Principles, Third Edition, McGraw-Hill, 2003</li> <li>• A. Malvino and D. Bates, Electronic Principles, 8th Edition, McGraw-Hill, 2016</li> </ul> |



|  |   |
|--|---|
|  | <ul style="list-style-type: none"><li>• M. E.Schultz, and B. Grob, Grob's Basic Electronics. 12th ed. McGraw-Hill, 2015.</li><li>• P.Y. Yu and M. Cardona, Fundamentals of Semiconductors: Physics and Materials Properties, Third edition, 2010</li><li>• S. M. Sze, M. Lee, Semiconductor Devices: Physics and Technology, 3rd Edition, Wiley, 2012</li></ul> |
|--|---|

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Software Engineering</b> |
| <b>Module Abbreviation</b> | ELE-B-2-3.02                |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler    |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 3rd Semester / Summer Semester / 1 Semester |
|--|---|

|   |  |
|---|--|
| <b>Qualification Objectives</b>               | <p>Students will acquire basic skills in software engineering:</p> <ul style="list-style-type: none"> <li>• After completing the lecture, students will have mastered the most important principles of Object-Oriented Analysis (OOA) and Object-Oriented Design (OOD).</li> <li>• They will understand the relevant UML description tools and will be able to use them.</li> <li>• Students will be able to name the different phases of the software development process and apply different methods of requirements engineering.</li> <li>• They will know the rules of teamwork and will be able to apply them in their groups.</li> </ul> <p>The theoretical and practical work in the fields of programming and software engineering make up the basis that will allow students to master and design software-intensive systems. These include, for example, autonomous systems, which are the subject matter of other courses such as Microcontrollers and Advanced Embedded Systems.</p> |
| <b>Content</b>                                | <ul style="list-style-type: none"> <li>• General basics of software engineering</li> <li>• Requirements Engineering and object-oriented analysis: basic terms, phases, activities and procedures</li> <li>• OOA and OOD with the UML including use cases, activity diagrams, class diagrams, state diagrams, sequence diagrams.</li> </ul>   |
| <b>Teaching Method</b>                        | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b> | <p>The lecture will be taught in the style of a seminar.</p> <p>The exercises are developed, amongst others, in teams and the solutions are preferably presented by the students</p>   |
| <b>Examination Forms</b>                      | Module exam as a written exam (90 min.).   |

|  |   |
|--|---|
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"><li>• G. Booch, J. Rumbaugh, and I. Jacobson, The unified modeling language user guide, 2nd ed. Upper Saddle River, NJ: Addison-Wesley, 2005.</li><li>• Sommerville, I.: Software Engineering (9. Ed.), Boston (USA): Pearson Education, 2011.</li><li>• Oshana, R.: Software Engineering for Embedded Systems: Methods, Practical Techniques, and Applications (Expert Guide), Newnes, Mai 2013, ISBN: 978-0124159174.</li></ul> |

|                            |                          |
|----------------------------|--------------------------|
| <b>Module Name</b>         | <b>Embedded Systems</b>  |
| <b>Module Abbreviation</b> | ELE-B-2-3.03             |
| <b>Module Coordinator</b>  | Prof. Dr. Achim Rettberg |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 10      | <b>Total Workload</b>  | 300 |
| <b>HPW</b>          | 6       | <b>Contact Hours</b>   | 90  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 210 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 3rd Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | <p>Students will acquire basic competencies in the construction and programming of embedded systems:</p> <ul style="list-style-type: none"> <li>• The students will know the application areas of embedded systems.</li> <li>• They will know the structure (core and interfaces) and the functionality of microprocessors and microcontrollers and they will be able to explain this and they will be able to select the right architecture for any given problem.</li> <li>• They will have practical experience with the development of software for embedded systems in the C and C++ programming languages.</li> <li>• The students will know the basic functionality of real-time operating systems.</li> <li>• Students will be able to design and test embedded systems by applying the analytical and technical methods of electronics and technical computer science as well as intercultural competencies they have learned in the bachelor course.</li> <li>• The students know the basic functioning of embedded computer networks.</li> </ul> <p>The theoretical and practical work will be a foundation for considering larger problems in the context of prototype work. In the practical part of the course, the students will work in small groups in the area of microcontroller programming, which will support the development of communication skills and agreement between students and will also increase their intercultural and social competencies.</p> |
| <b>Content</b>                  | <ul style="list-style-type: none"> <li>• Representation of information in the computer</li> <li>• Internal structure of a microprocessor</li> <li>• Structure and components of a microcontroller (counter / timer, A/D converter, watchdog)</li> </ul>  |

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• Basics of hardware-related software development for microprocessors and microcontrollers with C (data types, control structures, pointers, functions)</li> <li>• Modeling and implementation of control algorithms using finite state machines</li> <li>• Special features of hardware-related software development fundamentals of real-time operating systems</li> <li>• Interfaces (including <math>\mu</math>C interfaces, bus systems)</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw), Lab (2 hpw).  |
| <b>Course / Teaching and Learning Methods</b>      | In the lecture, the fundamentals will be explained, and examples will be discussed together. In the exercise and lab, the procedures will be demonstrated; tasks and projects will be completed and individual questions will be answered.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min.).   |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• Cady, Fredrick M.: Microcontrollers and Microcomputers: Principles of Software and Hardware Engineering, Oxford University Press, 1997.</li> <li>• Valvano, Jonathan W.: Embedded Systems: Introduction to Arm Cortex-M Microcontrollers, 5th Edition, CreateSpace Independent Publishing Platform, 2012, ISBN-10: 1477508996.</li> <li>• Zhu, Yifeng: Embedded Systems with ARM Cortex-M Microcontrollers in Assembly Language and C, E-Man Press LLC; 2 edition, 2015, ISBN-10: 0982692633.</li> <li>• Noergaard, Tammy: Embedded Systems Architecture, Second Edition: A Comprehensive Guide for Engineers and Programmers, Newnes; 2 edition, 2012, ISBN-10: 0123821967.</li> <li>• Kernighan, Brian W.; Ritchie Dennis M.: C Programming Language, Prentice Hall; 2 edition, 1988, ISBN-10: 0131103628.</li> <li>• Stroustrup, Bjarne: The C++ Programming Language, Addison-Wesley Professional; 4th edition, 2013, ISBN-10: 0321563840.</li> <li>• Stroustrup, Bjarne: Programming: Principles and Practice Using C++, Addison-Wesley Professional; 2nd edition, 2014, ISBN-10: 0321992784.</li> </ul> |

|                            |                                |
|----------------------------|--------------------------------|
| <b>Module Name</b>         | <b>Mathematics 3</b>           |
| <b>Module Abbreviation</b> | ELE-B-2-3.04                   |
| <b>Module Coordinator</b>  | Prof. Dr. Jan Eric Kyprianidis |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 3rd Semester / Summer Semester / 1 Semester |
|--|---|

|  |   |
|--|---|
| <b>Qualification Objectives</b>                    | Students acquire advanced skills in basic mathematical concepts and procedures. They can calculate partial derivatives and differential operators of vector analysis. They can calculate curve, surface and volume integrals and apply the integral theorems of Gauss and Stokes. For typical tasks in the field of technical systems they can select and apply the appropriate learned methods and interpret the results.                    |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Differential and integral calculus in several variables</li> <li>• Vector analysis</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw).  |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A. Croft et al., Engineering Mathematics, 5th ed. Pearson Education, 2017.</li> <li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li> <li>• G. James et al. Advanced Modern Engineering Mathematics, 4th ed. Prentice Hall, 2011.</li> <li>• D.G. Zill and W.S. Wright, Advanced Engineering Mathematics, 5th ed. Jones &amp; Bartlett Learning, 2014.</li> </ul> |

|                            |                                   |
|----------------------------|-----------------------------------|
| <b>Module Name</b>         | <b>Audio and Video Processing</b> |
| <b>Module Abbreviation</b> | ELE-B-2-3.05                      |
| <b>Module Coordinator</b>  | Prof. Stefan Albertz              |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 3       | <b>Contact Hours</b>   | 45  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 105 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 3rd Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | The students will understand image and audio signal processing, current audio and video coding methods as well as complex imaging systems. They will use existing processing methods and will be taught how to analyze new procedures, to apply them in their own projects and to further develop them.  |
| <b>Content</b>                  | <p>File-Based Workflows</p> <ul style="list-style-type: none"> <li>• Transition</li> <li>• Distribution</li> <li>• Security Features</li> </ul> <p>Mastering &amp; Distribution</p> <ul style="list-style-type: none"> <li>• Current Mastering Standards</li> <li>• Distribution Channels for A / V Media</li> <li>• Broadcast</li> <li>• Video on Demand (VoD, OTT)</li> <li>• Media Asset Management</li> </ul> <p>Digital Image Recording</p> <ul style="list-style-type: none"> <li>• Image Sensors</li> <li>• Bayer Pattern</li> <li>• Debayering</li> <li>• RAW Workflow</li> </ul> <p>A/V Measurement Procedures</p> <ul style="list-style-type: none"> <li>• Signal Level Measurement</li> <li>• Codec Analysis Tools</li> </ul> <p>Audio Compression</p> <ul style="list-style-type: none"> <li>• Fundamentals</li> <li>• Psychoacoustic Effects</li> <li>• MPEG Layer 3 / AAC</li> </ul> <p>Audio Reproduction</p> <ul style="list-style-type: none"> <li>• Frequency-Dependent Filters</li> <li>• Time-Dependent Filters</li> <li>• Object-Based Methods</li> </ul> |

|  |   |
|--|---|
|  | <p>Video Compression</p> <ul style="list-style-type: none"> <li>• Fundamentals</li> <li>• Redundancy in the Moving Image</li> <li>• Group of Pictures Methods</li> <li>• Motion Estimation</li> <li>• MPEG-2 Method</li> <li>• Generation Loss</li> </ul> <p>Image Reproduction Methods</p> <ul style="list-style-type: none"> <li>• Display Technology (in depth)</li> <li>• Projectors</li> </ul>   |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (1 hpw).  |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will take place in the seminar style, supplemented by case studies, individual and group work, presentations, reflection and feedback discussion.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• Dickreiter, M., Dittel, V., Hoeg, W., Wöhr, M. (2014): Handbuch der Tonstudioteknik - Band 1, De Gruyter, SAUR, ISBN 978-3-11-028978-7.</li> <li>• Dickreiter, M., Dittel, V., Hoeg, W., Wöhr, M. (2014): Handbuch der Tonstudioteknik - Band 2, De Gruyter, SAUR, ISBN 978-3-11-028978-7.</li> <li>• Friesecke, A. (2014): Die Audio-Enzyklopädie - ein Nachschlagewerk für Tontechniker, De Gruyter, SAUR, ISBN 978-3-11-034013-6.</li> <li>• Poynton, C.A.(2012): Digital Video and HD: Algorithms and Interfaces, Morgan Kaufmann, ASIN B00Y2QVVLA.</li> </ul> |



|                            |   |
|----------------------------|---|
| <b>Module Name</b>         | <b>Control Engineering 1</b>              |
| <b>Module Abbreviation</b> | ELE-B-2-4.01                              |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. João Paulo Javidi da Costa |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 4th Semester / Summer Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | Students acquire basic skills in the technical fundamentals of control engineering. They will be able to model control systems and explain the functionality of different types of controllers. They can examine control systems for stability and design controllers using MATLAB. For typical tasks in the field of technical systems they can select and apply the appropriate learned methods and interpret the results. |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Introduction to control engineering</li> <li>• Design of control systems</li> <li>• Types of controllers</li> <li>• Stability</li> <li>• Examples and introduction to MATLAB for the design of control systems.</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A J. Wilkie, M. Johnson and R. Katebi, Control Engineering - an Introductory Course, Palgrave Publisher, 2002</li> <li>• O. Katsuhiko. Modern Control Engineering. 5th ed. Pearson, 2010.</li> </ul>  |

|  |  |
|--|--|
|  | <ul style="list-style-type: none"><li>• O. Katsuhiko. MATLAB for Control Engineers. Pearson, 2008.</li><li>• R.C. Dorf and R.H. Bishop. Modern Control Systems. 13th ed. Pearson, 2017.</li><li>• C.C. Houpis, S.N. Sheldon: „Linear Control System Analysis and Design with Matlab, 6th Edition“, CRC Press 2013, ISBN: 9781466504264.</li><li>• Croft and R. Davison, Mathematics for Engineers, 5th ed. Pearson Education, 2017.</li><li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li><li>• G. James et al., Advanced Modern Engineering Mathematics, 4th ed. Prentice Hall, 2011.</li><li>• D.G. Zill and W.S. Wright, Advanced Engineering Mathematics, 6th ed. Jones &amp; Bartlett Learning, 2018.</li></ul> |
|--|--|

|                            |                               |
|----------------------------|-------------------------------|
| <b>Module Name</b>         | <b>Hardware Engineering 1</b> |
| <b>Module Abbreviation</b> | ELE-B-2-4.02                  |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. Ali Hayek      |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 4th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | <p>The course introduces the principles of the hardware engineering design process and gives an introduction into circuit design and the different methods and techniques to design hardware. Furthermore, it serves also as an introduction into the basics of circuit design and the creation process of printed circuit boards (PCBs).</p> <ul style="list-style-type: none"> <li>• The students will be able to explain the hardware design flow and differentiate between different component types which can be used for hardware design.</li> <li>• They will be able to select the right components, design and build circuit models for a dedicated hardware prototype using microcontrollers, ICs and other electronic components.</li> <li>• They will be able to develop a circuit for hardware prototypes using latest CAD software tools</li> <li>• The students will learn the methods, tools and procedures to design, partition and manufacture a system-specific electronic sub-assembly in SMT technology.</li> </ul> |
| <b>Content</b>                  | <ul style="list-style-type: none"> <li>• Hardware design flow</li> <li>• Electronic hardware components</li> <li>• System specification</li> <li>• Circuit design</li> <li>• Partitioning and layout design</li> <li>• Electronic assembly</li> <li>• Construction and manufacturing of PCBs</li> <li>• Fundamentals of Surface Mount Technology (SMT)</li> <li>• Design-to-Cost considerations</li> <li>• Aspects of Electromagnetic Compatibility (EMC)</li> <li>• Design tools and examples</li> </ul>  |
| <b>Teaching Method</b>          | Lecture (2 hpw), Exercise (2 hpw).   |

|  |   |
|--|---|
| <b>Course / Teaching and Learning Methods</b>      | The basics for the continuing engineering disciplines will be taught based on current practice examples and in relation to current topics. Exercises will be integrated into the lecture. A practical lab part will be implemented based on the content of the lecture.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• P. Scherz and S. Monk, Practical Electronics for Inventors, 4th Edition, McGraw-Hill Education Ltd, 2016</li> <li>• S. Monk, Make your own PCBs with Eagle, Publisher McGraw Hill, 2014</li> <li>• M. Scarpino, Designing circuit board with Eagle, Pearson, 2014</li> <li>• A. Williams, Build Your Own Printed Circuit Board, McGraw-Hill Publisher, 2004</li> <li>• Association Connecting Electronics Industries:<br/><a href="http://www.ipc.org/">http://www.ipc.org/</a></li> </ul> |

|                            |  |
|----------------------------|--|
| <b>Module Name</b>         | <b>Prototyping and Systems Engineering</b> |
| <b>Module Abbreviation</b> | ELE-B-2-4.03                               |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler                   |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 10      | <b>Total Workload</b>  | 300 |
| <b>HPW</b>          | 6       | <b>Contact Hours</b>   | 90  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 210 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 4th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p><b>Prototyping</b></p> <p>The students will acquire competencies in interdisciplinary group work:</p> <ul style="list-style-type: none"> <li>• Students will be able to apply the acquired competencies in the fields of electrical engineering, computer science and design to an interdisciplinary project.</li> <li>• They will gain an understanding of the interdisciplinary interaction in the conception and implementation of a complex task and, taking design parameters into account, will be able to select suitable methods and techniques from the various disciplines and apply them independently.</li> <li>• They will be able to write up the results in a scientific text using the principles of scientific writing.</li> <li>• Furthermore, students will be able to apply intercultural competencies by using the techniques discussed in the course to later develop systems in an international environment.</li> </ul> <p>The practical work serves as a basis for considering larger problems in the context of a thesis or project work. The interdisciplinary work in small groups strengthens the communication skills and the agreement between students.</p> <p><b>Systems Engineering</b></p> <p>The students will be familiar with the different levels of system engineering. This includes technical management and the path from system analysis and system design to product realization. The students will acquire knowledge about the classification of phase models and the interaction of the phases. The students will be able to apply UML and SysML and to use them for technical applications. They will be capable of making project plans for complex projects:</p> |
|---------------------------------|---|

|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• Students will be familiar with the challenges of developing systems with an interdisciplinary character.</li> <li>• They will know the terms, characteristics and definitions of systems and can explain phase models and the interaction of the project phases.</li> <li>• The students will be able to present sub-processes of system engineering and recognize the interaction between project management and system design.</li> <li>• They will be able to apply methods and techniques of requirement and risk management.</li> <li>• Students will be able to use SysML for technical applications in various project phases by using the methods and techniques of system engineering in order to design complex problems across systems.</li> </ul> <p>The knowledge acquired will be used in the Prototyping course. The theoretical and practical work during the course will serve as a foundation for considering larger issues within a thesis or project work and will give the students insight into the work of a systems engineer.</p> |
| <b>Content</b>                                     | <p><b>Prototyping:</b></p> <p>Students reflect and deepen the knowledge acquired at the university and apply it in an interdisciplinary application to design a prototype. The prototype is designed holistically at system level and considers the design, hardware and software of the system to be designed. (Intermediate) results are presented, considering the knowledge gained in the field of presentation techniques.</p> <p><b>Systems Engineering:</b></p> <ul style="list-style-type: none"> <li>• Characteristics and definition of systems</li> <li>• Modelling of Systems (e.g. SysML)</li> <li>• Life Cycle Models</li> </ul>   |
| <b>Teaching Method</b>                             | <p>Prototyping: Practice (4 hpw)</p> <p>Systems Engineering: Lecture (2 hpw)</p>   |
| <b>Course / Teaching and Learning Methods</b>      | <p>Individual and group work, presentations, reflection and feedback discussions.</p> <p>The lecture will be taught in the style of a seminar.</p>   |
| <b>Examination Forms</b>                           | <p>Examination within the scope of the practice: The students will independently complete a project. This includes writing a documentation in the range of 10 to 15 pages (40%) and a final presentation of 20 minutes (60%).</p>  |
| <b>Participation Recommendations</b>               | <p>Successful participation in the basic courses of electrical engineering, computer science and mathematics.</p>  |
| <b>Prerequisite for the award of ECTS credits</b>  | <p>Passed final module exam.</p>   |
| <b>Use of the module (in other study programs)</b> | <p>-</p>   |

|                                  |   |
|----------------------------------|---|
| <b>Bibliography / Literature</b> | <p>Fundamental literature from the various disciplines as well as from the area of Systems Engineering.</p> <p>Systems Engineering</p> <ul style="list-style-type: none"><li>• T. Weilkens, Systems Engineering with SysML/UML. 2011.</li><li>• INCOSE technical board, "Systems Engineering Handbook", Version 4 INCOSE, 2015.</li><li>• Friedenthal, S.; Moore, A.; Steiner, R.: A Practical Guide to</li><li>• SysML: The Systems Modeling Language, Morgan Kaufmann, 2nd Edition, October 2011, ISBN: 978-0123852069.</li></ul> |
|----------------------------------|---|

|                            |                                |
|----------------------------|--------------------------------|
| <b>Module Name</b>         | <b>Mathematics 4</b>           |
| <b>Module Abbreviation</b> | ELE-B-2-4.04                   |
| <b>Module Coordinator</b>  | Prof. Dr. Jan Eric Kyprianidis |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 4th Semester / Summer Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | Students acquire advanced skills in basic mathematical concepts and procedures. They can calculate Laplace and Fourier transformations. They can solve linear differential equations with constant coefficients and systems of linear differential equations, using the Laplace transform. They can solve basic problems of probability and statistics. For typical tasks in the field of technical systems they can select and apply the appropriate learned methods and interpret the results. |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Laplace Transform</li> <li>• Fourier Transform</li> <li>• Probability and statistics</li> </ul>   |
| <b>Teaching Method</b>                             | Lecture (2 hpw), Exercise (2 hpw).   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises, relevant tasks will be completed, and the results will be discussed. Moreover, experiments related to electric circuits will be conducted.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A. Croft et al., Engineering Mathematics, 5th ed. Pearson Education, 2017.</li> <li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li> </ul>  |



|  |   |
|--|---|
|  | <ul style="list-style-type: none"><li>• G. James et al. Advanced Modern Engineering Mathematics, 4th ed. Prentice Hall, 2011.</li><li>• D.G. Zill and W.S. Wright, Advanced Engineering Mathematics, 5th ed. Jones &amp; Bartlett Learning, 2014.</li></ul> |
|--|---|

|                            |                               |
|----------------------------|-------------------------------|
| <b>Module Name</b>         | <b>Business Communication</b> |
| <b>Module Abbreviation</b> | ELE-B-2-4.05                  |
| <b>Module Coordinator</b>  | Dr. Birte Horn                |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 3       | <b>Contact Hours</b>   | 45  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 105 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 4th Semester / Summer Semester / 1 Semester |
|--|---|

|   |  |
|---|--|
| <b>Qualification Objectives</b>                   | Through the practical application and strengthening of general language knowledge as well as the acquisition of subject-specific technical terms, students will be able to adequately communicate and correspond in the English language during their studies and in their future careers. The focus lies on communication in specific business situations and environments. Students will also strengthen intercultural competencies that they can use in various communication scenarios by engaging with the characteristics of different cultures in their careers. Students will obtain the required knowledge to submit application documents in English and interview for a job in English. |
| <b>Content</b>                                    | <ul style="list-style-type: none"> <li>• Subject-specific development of language skills</li> <li>• Basics business English and commercial technical terminology</li> <li>• Editing and writing their own short texts</li> <li>• Oral and written communication</li> <li>• Intercultural Skills</li> <li>• Job applications</li> <li>• Review academic writing</li> </ul>  |
| <b>Teaching Method</b>                            | Seminar (3 hpw).   |
| <b>Course / Teaching and Learning Methods</b>     | Seminar-style teaching, lectures, case studies, individual and group work, presentations, reflection and feedback discussions.   |
| <b>Examination Forms</b>                          | Module exam as a presentation (20 minutes, 40%) with subsequent paper (5 - 7 pages, 30%); short academic essay (c. 3 pages, 30%).  |
| <b>Participation Recommendations</b>              | None.  |
| <b>Prerequisite for the award of ECTS credits</b> | Passed final module exam.  |

|   |   |
|---|---|
| <p><b>Use of the module (in other study programs)</b></p> | <p>-</p>  |
| <p><b>Bibliography / Literature</b></p>                   | <ul style="list-style-type: none"> <li>• Butzphal, Gerlinde; Maier-Fairclough, Jane: Career-Express. Business English: B2 Kursbuch mit Hör-CDs und Phrasebook. Berlin: Cornelsen, 2010.</li> <li>• Walker, Carolyn; English for Business Studies in Higher Education; Reading: Garnet Publishing, 2008.</li> <li>• Downes, Colm: Cambridge English for Job-hunting; Cambridge: CUP, 2008.</li> <li>• Schürmann, Klaus; Mullins; Suzanne: Die perfekte Bewerbungsmappe auf Englisch. Anschreiben, Lebenslauf und Bewerbungsformular länderspezifische Tipps. Frankfurt/Main: Eichborn, 2012.</li> <li>• Lewis, Richard D.; When Cultures Collide. Leading Across Cultures; Boston: Nicholas Brealey Int., 2006.</li> <li>• Dignen, Bob; Communicating Across Cultures; Cambridge: CUP, 2011.</li> <li>• Dignen, Bob und Wollmann, Peter. Leading International Projects; London: KoganPage, 2016.</li> <li>• Dignen, Bob with Chamberlain, James; Fifty Ways to improve your Intercultural Skills; Summertown Publishing, 2009.</li> </ul> |

|                            |                                       |
|----------------------------|---------------------------------------|
| <b>Module Name</b>         | <b>Internship / Exchange Semester</b> |
| <b>Module Abbreviation</b> | ELE-B-2-5.01                          |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler              |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 30      | <b>Total Workload</b>  | 900 |
| <b>HPW</b>          | -       | <b>Contact Hours</b>   | 10  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 890 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 5th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | <p>This module enables students to apply the skills they have acquired so far and, in addition, to orient themselves for the further course of their studies. The acquisition of the following skills is to be promoted:</p> <ul style="list-style-type: none"> <li>• intercultural competencies</li> <li>• instrumental competences by applying the acquired knowledge in professional practice</li> <li>• acquisition of professional experience</li> <li>• professional field orientation</li> <li>• deepening of scientific qualifications</li> <li>• self-reflection</li> <li>• Impulses for further studies</li> </ul> <p>The focus can be optionally on a deepening of the acquired knowledge in the concrete application in professional practice and/or in the promotion of intercultural competence through a stay abroad. The modules in the area of soft-skills form the basis for this.</p> |
| <b>Content</b>                  | <p><b>Internship in the domestic industrial company:</b></p> <ul style="list-style-type: none"> <li>• Students choose specific tasks outside the university, which are characterized by the practical cooperation in various operational areas result.</li> <li>• Ideally, students belong to a team with a fixed area of responsibility. Within this framework, they take on clearly defined tasks or subtasks and are thus given the opportunity to classify the significance of the individual tasks in the context of the overall operations.</li> <li>• Support from a supervisor from the university</li> <li>• Learning location: company, business enterprise, research institute, university, public authority, association, etc.</li> </ul> <p><b>University semester or internship in an industrial company abroad:</b></p>   |

|  |   |
|--|---|
|  | <ul style="list-style-type: none"> <li>• The contents of the internship at an industrial company abroad are comparable to those in Germany.</li> <li>• In addition, the deepening of the intercultural competence is an additional focus.</li> <li>• If a university semester is spent abroad, the completion of defined study elements is a main focus.</li> <li>• Support by a supervisor of the university</li> <li>• Learning location: university, company, business enterprise, research institute, public authority, association, etc. abroad</li> </ul> |
| <b>Teaching Method</b>                             | Application oriented work   |
| <b>Course / Teaching and Learning Methods</b>      | Application oriented Work   |
| <b>Examination Forms</b>                           | <p>Module final examination as practical report (10 to 15 pages, 80%) and a presentation (10 to 15 minutes, 20%) or the proof of passed examinations at the foreign cooperation university*.</p> <p>* is defined in the Learning Agreement.</p>   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | Internship agreement of Hamm-Lippstadt University of Applied Sciences and bibliography from module scientific work.   |

|                            |   |
|----------------------------|---|
| <b>Module Name</b>         | <b>Control Engineering 2</b>              |
| <b>Module Abbreviation</b> | ELE-B-2-6.01                              |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. João Paulo Javidi da Costa |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 6th Semester / Summer Semester / 1 Semester |
|--|---|

|  |  |
|--|--|
| <b>Qualification Objectives</b>                    | Students acquire advanced skills in the technical fundamentals of control engineering. They will be able to model control systems in state space. They can linearize nonlinear state variable model and design state variable controller structures using MATLAB. For typical tasks in the field of technical systems they can select and apply the appropriate learned methods and interpret the results. |
| <b>Content</b>                                     | <ul style="list-style-type: none"> <li>• Root Locus analysis</li> <li>• Controller design using the Bode plot</li> <li>• Nichols chart</li> <li>• PID controller tuning methods</li> <li>• State variable</li> <li>• Linearization of nonlinear systems</li> <li>• Control systems design in state space</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 HPW), Exercise (2 HPW).   |
| <b>Course / Teaching and Learning Methods</b>      | The lecture will be taught in the style of a seminar. In the exercises relevant tasks will be completed and the results will be discussed.   |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 minutes).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• A J. Wilkie, M. Johnson and R. Katebi, Control Engineering - an Introductory Course, Palgrave Publisher, 2002</li> <li>• O. Katsuhiko. Modern Control Engineering. 5th ed. Pearson, 2010.</li> </ul>  |

|  |   |
|--|---|
|  | <ul style="list-style-type: none"><li>• O. Katsuhiko. MATLAB for Control Engineers. Pearson, 2008.</li><li>• R.C. Dorf and R.H. Bishop. Modern Control Systems. 13th ed. Pearson, 2017.</li><li>• C.C. Houppis, S.N. Sheldon: „Linear Control System Analysis and Design with Matlab, 6th Edition“, CRC Press 2013, ISBN: 9781466504264.</li><li>• Croft and R. Davison, Mathematics for Engineers, 5th ed. Pearson Education, 2017.</li><li>• G. James et al., Modern Engineering Mathematics, 5th ed. Prentice Hall, 2005.</li><li>• G. James et al., Advanced Modern Engineering Mathematics, 4th ed. Prentice Hall, 2011.</li><li>• D.G. Zill and W.S. Wright, Advanced Engineering Mathematics, 6th ed. Jones &amp; Bartlett Learning, 2018.</li></ul> |
|--|---|

|                            |                               |
|----------------------------|-------------------------------|
| <b>Module Name</b>         | <b>Hardware Engineering 2</b> |
| <b>Module Abbreviation</b> | ELE-B-2-6.02                  |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. Ali Hayek      |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 5       | <b>Total Workload</b>  | 150 |
| <b>HPW</b>          | 4       | <b>Contact Hours</b>   | 60  |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 90  |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 6th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p>The course covers the hardware design of digital circuits. Students will acquire in-depth competencies in hardware-related implementation and theoretical knowledge, in order to apply structural and behavioral design techniques for the implementation of functions:</p> <ul style="list-style-type: none"> <li>• Students will be able to explain techniques for transitioning from logic to switching algebra, differentiating the relationship between design parameters (performance, area, power consumption, costs) and switching algebra processes.</li> <li>• Students will be able to explain digital combinational and sequential circuits e.g. Arithmetic Logic Units and Mealy &amp; Moore machines, the building blocks of digital technology, VHDL language elements and HW technologies.</li> <li>• Students will be able to minimize switching functions, design sequential circuits, create simple VHDL programs, configure an FPGA device, and implement functions on their own.</li> <li>• Students will be able to apply the acquired skills in digital technology to a practical work based on FPGAs. In the development of the project, intercultural requirements will be considered in addition to the technical issues.</li> </ul> |
| <b>Content</b>                  | <ul style="list-style-type: none"> <li>• Digital circuitry and interfaces</li> <li>• Terms, classes, presentation forms (tabular, graphical, algebraic)</li> <li>• Normal forms (CNF, DNF)</li> <li>• Logic minimization</li> <li>• Combinatorial logic</li> <li>• Sequential logic</li> <li>• Syntax and semantics of Hardware Description Languages (VHDL)</li> <li>• Sequential circuits &amp; Finite State Machines (FSMs)</li> <li>• Building blocks of digital technology</li> <li>• Design of digital circuits</li> </ul>  |



|  |  |
|--|--|
|  | <ul style="list-style-type: none"> <li>• Design of state machines</li> <li>• Simulation of hardware descriptions</li> <li>• Hardware design in FPGA technology</li> </ul>  |
| <b>Teaching Method</b>                             | Lecture (2 HPW), Lab (2 HPW).  |
| <b>Course / Teaching and Learning Methods</b>      | The basics for the continuing engineering disciplines will be taught based on current practice examples and in relation to current topics. Exercises will be integrated into the lecture. A practical lab part will be implemented based on the content of the lecture.  |
| <b>Examination Forms</b>                           | Module exam as a written exam (90 min).  |
| <b>Participation Recommendations</b>               | None.  |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.  |
| <b>Use of the module (in other study programs)</b> | -  |
| <b>Bibliography / Literature</b>                   | <ul style="list-style-type: none"> <li>• B. J. Lameris, "Introduction to Logic Circuits &amp; Logic Design with VHDL, 2d Edition, Springer Publisher, 2019</li> <li>• V. A. Pedroni, "Circuit Design with VHDL, MIT Press, 2004.</li> <li>• P. J. Ashenden, "The VHDL Cookbook," First Edition, 1990</li> <li>• R. E. Haskell and D. M. Hanna, Digital Design Using Digilent FPGA Boards: VHDL / Vivado Edition, LBE Books, 2019</li> <li>• D. M. Harris and S. L. Harris, Digital design and computer architecture, 2nd Edition, Morgan Kaufmann, 2013</li> <li>• V. Tarate, Logic Synthesis and SOC Prototyping: RTL Design using VHDL, 1st Edition, Springer, 2020</li> </ul> |

|                            |                          |
|----------------------------|--------------------------|
| <b>Module Name</b>         | <b>Project Work</b>      |
| <b>Module Abbreviation</b> | ELE-B-2-6.03             |
| <b>Module Coordinator</b>  | Prof. Dr. Achim Rettberg |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 8       | <b>Total Workload</b>  | 240 |
| <b>HPW</b>          | -       | <b>Contact Hours</b>   | -   |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 240 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 6th Semester / Summer Semester / 1 Semester |
|--|---|

|  |   |
|--|---|
| <b>Qualification Objectives</b>                    | <p>Students are able to apply the knowledge they have gained up to now in the course of their studies in concrete applications, e.g. professional practice. The students can use the concepts and methods learned to independently analyze a task, abstract its contents, structure the contexts and find and develop different solutions in the domain of electronic systems (e.g. hardware and/or software).</p> <p>The students are able to integrate the individual tasks, e.g. within a company, in superordinate objective and organizational contexts.</p> |
| <b>Content</b>                                     | Implementation of a project, which consists of the processing of a theoretical or practical task, with the aim of solving practical problems using scientific methods.  |
| <b>Teaching Method</b>                             | Project engineering.  |
| <b>Course / Teaching and Learning Methods</b>      | <p>Working on a project with supporting professional discussion with the supervising professor.</p> <p>Self-organized learning, individual or work.</p>   |
| <b>Examination Forms</b>                           | Module final examination as a project work, including a scientific paper (15 - 30 pages, 80%) and a presentation (10 - 15 minutes, 20%).  |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |

|                                  |   |
|----------------------------------|---|
| <b>Bibliography / Literature</b> | Subject-specific, independent literature research with the support by the supervisor. |
|----------------------------------|---|

|                            |   |
|----------------------------|---|
| <b>Module Name</b>         | <b>Bachelor Thesis incl. Colloquium</b> |
| <b>Module Abbreviation</b> | ELE-B-2-7.01                            |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler                |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 15      | <b>Total Workload</b>  | 450 |
| <b>HPW</b>          | -       | <b>Contact Hours</b>   | -   |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 450 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 7th Semester / Summer Semester / 1 Semester |
|--|---|

|   |   |
|---|---|
| <b>Qualification Objectives</b>               | <p>Students will be able to independently solve a complex task of their subject area independently with the help of scientific methods, solve this task within a given time frame and evaluate this solution.</p> <p>They are able to evaluate the state of the art and research, solution concepts, technical concepts, systems and setups, design drafts, developed hardware and software, achieved results, possible extensions and evaluations in a scientific way. They can present the results in a scientific written and oral form.</p> |
| <b>Content</b>                                | Processing of a theoretical or practical task / research challenge to solve problems with the help of scientific methods.   |
| <b>Teaching Method</b>                        | Bachelor thesis including bachelor seminar  |
| <b>Course / Teaching and Learning Methods</b> | <p>Bachelor's thesis: Independent work and discussions with supervising Professor.</p> <p>Bachelor seminar: Presentation and discussion.</p> <p>Self-organized learning, individual or group work.</p>  |
| <b>Examination Forms</b>                      | <p>Module final examination as written part (bachelor thesis, guideline: 30 to 60 pages, 80%) and oral part (Bachelor seminar, 20 to 30 minutes presentation and oral examination, 20%).</p> <p>In individual cases, the standard values may be deviated from.</p> <p>Proportion of the examination performance in the overall grade:</p> <ul style="list-style-type: none"> <li>• Bachelor thesis: 4/5 (12 ECTS points)</li> <li>• Bachelor seminar: 1/5 (3 ECTS points)</li> </ul>  |
| <b>Participation Recommendations</b>          | None.   |

|  |   |
|--|---|
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | Subject-specific, independent literature research with the support by the supervisor. |

## **Special Emphasis A**

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Autonomous Systems A</b> |
| <b>Module Abbreviation</b> | ELE-B-2-6.04                |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler    |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 12      | <b>Total Workload</b>  | 360 |
| <b>HPW</b>          | 8       | <b>Contact Hours</b>   | 120 |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 240 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 6th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |  |
|---------------------------------|--|
| <b>Qualification Objectives</b> | <p><b>Cyber-Physical Systems</b></p> <p>The students will acquire in-depth competencies in the development of networked, technical systems:</p> <ul style="list-style-type: none"> <li>• Students will be able to explain the challenges and characteristics of cyber-physical systems.</li> <li>• They will be able to explain specification and modeling techniques of cyber-physical systems and decide when to use which techniques.</li> <li>• The students will be familiar with the various protocols for networked systems and can apply them in the application context.</li> </ul> <p><b>Machine Learning</b></p> <p>The students will acquire in-depth competencies in the application of machine learning algorithms.</p> <ul style="list-style-type: none"> <li>• Students will be able to explain the challenges and characteristics of machine learning.</li> <li>• Students will be able to explain the different types of machine learning algorithms including their characteristics.</li> <li>• Students can apply machine learning algorithms in a given context in an application-specific manner. The application involves information coding and preprocessing as part of the algorithm.</li> </ul> <p><b>Autonomous Systems A Lab</b></p> <p>The students will be able to apply the concepts and methods of the courses Cyber-physical Systems and Machine Learning in a systematic and combined way to solve a given problem. The students are able to design a networked technical system by using the design techniques of Cyber-Physical Systems and to refine this to a technical implementation.</p> <p>During the practical course, they will be able to write a scientific text based on the principles of scientific writing.</p> |
|---------------------------------|--|

|   |  |
|---|--|
|   | The theoretical and practical work will serve as a foundation for the consideration of large problems in the context of a thesis or project work. The work in small groups in the practical part in the design and analysis of cyber-physical systems will strengthen the ability of the students to communicate and coordinate.   |
| <b>Content</b>                                    | <p><b>Cyber-Physical Systems</b></p> <p>Basics and Definitions</p> <ul style="list-style-type: none"> <li>• Embedded Systems Hardware and Software Architecture</li> <li>• Real-Time Operating Systems</li> </ul> <p>Specification and Modeling Techniques</p> <ul style="list-style-type: none"> <li>• Requirements</li> <li>• Communicating Finite State Machines</li> <li>• Data Flow</li> </ul> <p>Distributed Systems</p> <ul style="list-style-type: none"> <li>• Computer Networks</li> <li>• Bus Systems (various application domains)</li> <li>• Internet of Things</li> </ul> <p><b>Machine Learning</b></p> <ul style="list-style-type: none"> <li>• Motivation and Biological Foundations</li> <li>• Information Modeling</li> <li>• Basics of decision trees, classification, and clustering</li> <li>• Optimal Learning</li> <li>• Feed-Forward Networks</li> <li>• Industrial Applications</li> </ul> <p><b>Autonomous Systems A Lab</b></p> <ul style="list-style-type: none"> <li>• Based on the methods and techniques learned in Cyber Physical Systems and Machine Learning, a project will be completed in the field of autonomous systems.</li> <li>• Independent planning, analysis, modeling, implementation and testing of a complex application example</li> </ul> |
| <b>Teaching Method</b>                            | <p>Cyber-Physical Systems: Seminar (2 hpw)</p> <p>Machine Learning: Seminar (2 hpw)</p> <p>Autonomous Systems A Lab: Practical work (4 hpw)</p>  |
| <b>Course / Teaching and Learning Methods</b>     | Seminar-style teaching, lectures, case studies, individual and group work, presentations, reflection and feedback discussions.   |
| <b>Examination Forms</b>                          | Module exam as a presentation (15 - 20 minutes, 2 times 5%) with subsequent paper (5 - 7 pages, 2 times 20%) for each seminar (seminars at all 50%) and examinations in the context of the lab: the students will independently complete a project. This includes writing a documentation of 2 to 5 pages (25%) and a final presentation of 10 to 15 minutes (25%).  |
| <b>Participation Recommendations</b>              | None.  |
| <b>Prerequisite for the award of ECTS credits</b> | Passed final module exam.  |



|   |   |
|---|---|
| <p><b>Use of the module (in other study programs)</b></p> | <p>-</p>  |
| <p><b>Bibliography / Literature</b></p>                   | <p>Cyber-Physical Systems</p> <ul style="list-style-type: none"> <li>• Lee, Edward A.; Seshia, Sanjit A.: Introduction to Embedded Systems: A Cyber-Physical Systems Approach, Introduction to Embedded Systems, 2nd Edition, 2016, ISBN-10: 0262533812.</li> <li>• Alur, Rajeev: Principles of Cyber-Physical Systems, Principles of Cyber-Physical Systems, 2015, ISBN-10: 0262029111.</li> <li>• Marvedel, Peter, Embedded System Design: Embedded Systems Foundations of Cyber-Physical Systems and the Internet of Things, Springer, 2017.</li> <li>• George Coulouris, Jean Dollimore, Tim Kindberg, Gordon Blair: Distributed Systems: Concepts and Design, Pearson, 5th edition, 2011, ISBN-10: 0132143011.</li> <li>• Tanenbaum, Andrew S., Van Steen, Maarten: Distributed Systems: Principles and Paradigms, CreateSpace Independent Publishing Platform, 2nd edition, 2016, ISBN-10: 153028175X.</li> <li>• Burns, Brendan: Designing Distributed Systems: Patterns and Paradigms for Scalable, Reliable Services, O'Reilly Media, 1st edition, 2017, ISBN-10: 1491983647.</li> <li>• P. P. Angelov, Autonomous learning systems: From data streams to knowledge in real-time.</li> <li>• A. Cardon and M. Itmi, New autonomous systems. London, UK, s.l.: ISTE Hoboken NJ, 2016.</li> <li>• D. D. Guinard and V. M. Trifa, Building the web of things: With examples in Node.js and Raspberry Pi. Shelter Island, NY: Manning Publications, 2016.</li> <li>• C. Hughes and T. Hughes, Robot programming: A guide to controlling autonomous robots. Indianapolis, Indiana: Que, 2016.</li> </ul> <p>Machine Learning</p> <ul style="list-style-type: none"> <li>• C. Bishop: "Pattern Recognition and Machine Learning", Springer Verlag 2006, ISBN: 978-0-387-31073-2.</li> <li>• C. Lau: "Neural Networks: Theoretical Foundations and Analysis", IEEE Press 1992, ISBN 10: 0879422807.</li> <li>• R. Schalkoff: "Pattern Recognition: Statistical, Structural and Neural Approaches", John Wiley &amp; Sons, Inc., 1992, ISBN: 0471529745.</li> <li>• R. O. Duda, P. E. Hart, D. G. Stork: "Pattern Classification", 2nd edition, John Wiley &amp; Sons, Inc., 2000, ISBN: 978-0-471-05669- 0.</li> <li>• Goodfellow, Ian; Bengio, Yoshua; Courville, Aaron: Deep Learning (Adaptive Computation and Machine Learning), The MIT Press, 2016, ISBN-10: 0262035618.</li> <li>• M. T. Hagan und H. B. Demuth: Neural Network Design. 2nd Edition.hagan.okstate.edu/NNDesign.pdf</li> <li>• <a href="http://www.deeplearningbook.org">www.deeplearningbook.org</a></li> <li>• <a href="https://developer.nvidia.com/deep-learning">https://developer.nvidia.com/deep-learning</a></li> </ul> |

|                            |  |
|----------------------------|--|
| <b>Module Name</b>         | <b>Embedded Electronic Engineering A</b> |
| <b>Module Abbreviation</b> | ELE-B-2-6.05                             |
| <b>Module Coordinator</b>  | Prof. Dr. Achim Rettberg                 |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 12      | <b>Total Workload</b>  | 360 |
| <b>HPW</b>          | 8       | <b>Contact Hours</b>   | 120 |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 240 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 6th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p><b>Real-Time Systems</b></p> <p>In many technical systems, the correctness of a result also depends on the point in time when the result was computed. The students will learn skills to analyze and design real-time systems:</p> <ul style="list-style-type: none"> <li>• The students will know the essential terms and definitions of real-time systems.</li> <li>• They will be familiar with real-time operating systems and their properties.</li> <li>• They will be familiar with aperiodic and periodic scheduling algorithms and will be to apply them after analyzing the application problem.</li> <li>• They will be familiar with the basics of worst-case execution time analysis.</li> </ul> <p><b>Hardware / Software Codesign</b></p> <p>Students will acquire in-depth competencies in design methods of hardware / software codesign:</p> <ul style="list-style-type: none"> <li>• They will be able to explain methods of hardware / software codesign.</li> <li>• They will be familiar with system partitioning approaches and will be able to apply them.</li> <li>• Students will be familiar with system analysis approaches and can design and simulate functions in VHDL or SystemC.</li> </ul> <p><b>Embedded Electronic Engineering A Lab</b></p> <p>Students will be able to independently design an application with consideration for real-time parameters and hardware / software codesign characteristics by applying scheduling and worst-case execution time methods and techniques to implement safety-critical systems. They will be able to present the results as part of the practical work in a scientific paper using the principles of scientific writing.</p> |
|---------------------------------|---|

|   |   |
|---|---|
|   | The theoretical and practical work will give students insight into the work of a hardware developer. The practical work in small groups will strengthen the communication and coordination skills of the students.  |
| <b>Content</b>                                    | <p><b>Real-Time Systems</b></p> <ul style="list-style-type: none"> <li>• Basics of Real-Time Systems</li> <li>• Aperiodic Scheduling Algorithms</li> <li>• Periodic Scheduling Algorithms</li> <li>• Real-Time Operating Systems and Standards</li> <li>• Real-Time Communication</li> </ul> <p><b>Hardware / Software Codesign</b></p> <p>System Partitioning</p> <ul style="list-style-type: none"> <li>• Levels of Abstraction</li> <li>• Cost Functions</li> <li>• Partitioning Methods</li> </ul> <p>System Simulation</p> <ul style="list-style-type: none"> <li>• System and Model</li> <li>• Discrete and Continuous State</li> <li>• Time Models</li> <li>• Discrete Event Simulation</li> </ul> <p>Syntax and Semantics of Hardware Description Languages</p> <ul style="list-style-type: none"> <li>• Simulation of Hardware Descriptions</li> <li>• Design of Digital Circuits</li> <li>• Design of State Machines</li> </ul> <p><b>Embedded Electronic Engineering A Lab</b></p> <ul style="list-style-type: none"> <li>• Based on the methods and techniques learned in Real-Time Systems and Hardware / Software Codesign, a project will be completed in the field of embedded systems.</li> <li>• Independent planning, analysis, modeling, implementation and testing of a complex application example</li> </ul> |
| <b>Teaching Method</b>                            | <p>Real-Time Systems: Seminar (2 hpw)</p> <p>Hardware / Software Codesign: Seminar (2 hpw)</p> <p>Electronic Engineering A Lab: Practical work (4 hpw)</p>  |
| <b>Course / Teaching and Learning Methods</b>     | Seminar-style teaching, lectures, case studies, individual and group work, presentations, reflection and feedback discussions.  |
| <b>Examination Forms</b>                          | Module exam as a presentation (15 - 20 minutes, 2 times 10%) with subsequent paper (5 - 7 pages, 2 times 15%) for each seminar (seminars at all 50%) and examinations in the context of the lab: the students will independently complete a project. This includes writing a documentation of 2 to 5 pages (25%) and a final presentation of 10 to 15 minutes (25%).  |
| <b>Participation Recommendations</b>              | None.   |
| <b>Prerequisite for the award of ECTS credits</b> | Passed final module exam.   |

|   |   |
|---|---|
| <p><b>Use of the module (in other study programs)</b></p> | <p>-</p>  |
| <p><b>Bibliography / Literature</b></p>                   | <p>Real-Time Systems</p> <ul style="list-style-type: none"> <li>• G. C. Buttazzo, Hard real-time computing systems: Predictable scheduling algorithms and applications, 3rd ed. New York, NY: Springer, 2011.</li> <li>• H. Kopetz, Real-time systems: Design principles for distributed embedded applications, 2nd ed. New York: Springer US, 2011.</li> <li>• D. Abbott, Linux for embedded and real-time applications, 3rd ed. Oxford: Newnes, 2013.</li> <li>• B. Selic and S. Gérard, Modeling and analysis of real-time and embedded systems with UML and MARTE: Developing cyber- physical systems. Amsterdam: Elsevier Morgan Kaufmann, 2014.</li> <li>• Valentini, M. Khalgui, and O. Mosbahi, Eds., Embedded computing systems: Applications, optimization, and advanced design. Hershey, Pa: IGI Global (701 E. Chocolate Avenue Hershey Pennsylvania 17033 USA), 2013.</li> </ul> <p>Hardware / Software Codesign</p> <ul style="list-style-type: none"> <li>• Schaumont, Patrick: A Practical Introduction to Hardware/Software Codesign, Springer, 2nd edition, 2014, ISBN-10: 1489990607.</li> <li>• Harris, David; Harris, Sarah: Digital Design and Computer Architecture, Morgan Kaufmann, 2nd edition, 2014, ISBN-10:0123944244.</li> <li>• Giovanni De Micheli, Rolf Ernst, and Wayne Wolf: Readings in Hardware/Software Co-Design. Morgan Kaufman, 2001.</li> <li>• Peter Marwedel: Embedded System Design. Springer, ISBN 978-94-007-0256-1, 2011.</li> <li>• Black, David C.: SystemC: From the Ground Up, Springer, 2nd edition, 2014, ISBN-10: 1489982663.</li> </ul> |

## **Special Emphasis B**

|                            |                             |
|----------------------------|-----------------------------|
| <b>Module Name</b>         | <b>Autonomous Systems B</b> |
| <b>Module Abbreviation</b> | ELE-B-2-7.02                |
| <b>Module Coordinator</b>  | Prof. Dr. Stefan Henkler    |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 15      | <b>Total Workload</b>  | 450 |
| <b>HPW</b>          | 10      | <b>Contact Hours</b>   | 150 |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 300 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 7th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p><b>Verification and Validation of Cyber-Physical Systems</b></p> <p>Cyber-Physical Systems are essentially distributed (technical) systems with a high degree of reliability. During the course students will acquire in-depth competencies in the reliability of software-intensive, technical systems:</p> <ul style="list-style-type: none"> <li>• Students will be able to explain the characteristics of cyber-physical systems.</li> <li>• They will be able to explain the challenges of reliable systems (especially security and confidentiality).</li> <li>• They will be able to explain modeling and analysis techniques for reliable systems and decide when to use which techniques.</li> </ul> <p><b>Deep Learning</b></p> <p>The course deepens the fundamentals of pattern recognition and machine learning. Students learn complex concepts and algorithms for the design of feedback neural networks for recognition tasks as well as simulation systems and hardware architectures in order to derive recommendations for the appropriate application of deep learning algorithms.</p> <p><b>Distributed Systems</b></p> <p>Students will be capable of designing distributed, technical systems:</p> <ul style="list-style-type: none"> <li>• They will understand the special requirements and challenges in developing distributed systems.</li> <li>• They will be familiar with the principles, architectures and Mechanisms of distributed systems.</li> <li>• They will be familiar with approaches to developing distributed systems systematically.</li> </ul> <p><b>Autonomous Systems B Lab</b></p> <p>Students will demonstrate in a semester project that applications from the area of embedded systems are reliable by using methods, techniques and tools to ensure safety, security, availability and</p> |
|---------------------------------|---|

|                               |   |
|-------------------------------|---|
|                               | <p>reliability. Furthermore, they will be able to apply deep learning algorithms by considering the characteristics of the given system context.</p> <p>They will be able to present the results as part of the practical work in a scientific paper using the principles of scientific writing.</p> <p>The theoretical and practical work will give students insight into the work of a hardware developer. The practical work in small groups will strengthen the communication and coordination skills of the students.</p>  |
| <p><b>Content</b></p>         | <p><b>Verification and Validation of Cyber-Physical Systems</b></p> <p>Basics and Definitions</p> <ul style="list-style-type: none"> <li>• Cyber-Physical Systems</li> <li>• Reliable Systems</li> </ul> <p>Modeling Techniques</p> <ul style="list-style-type: none"> <li>• Time-Dependent Automata</li> <li>• System Modeling Techniques</li> </ul> <p>Architectures for Reliable Systems</p> <ul style="list-style-type: none"> <li>• Fault-Tolerant Architectures</li> <li>• Safety-Critical Hardware</li> </ul> <p>Analysis Techniques</p> <ul style="list-style-type: none"> <li>• Hazard Analysis</li> <li>• Risk Analysis</li> <li>• Verification and Validation</li> </ul> <p><b>Deep Learning</b></p> <ul style="list-style-type: none"> <li>• Introduction to Neural Network Computing</li> <li>• Feedback Networks</li> <li>• Time Sequences</li> <li>• Genetic Algorithms</li> <li>• Simulation Systems, Software and Hardware Platforms for Neural Networks</li> </ul> <p><b>Distributed Systems:</b></p> <ul style="list-style-type: none"> <li>• Scenarios of Distributed Systems</li> <li>• Foundations of Distributed Systems</li> <li>• Distributed Data Management</li> <li>• Communication in Distributed Systems</li> <li>• Challenges of Distributed Systems</li> <li>• Quality of Distributed Systems (e.g. safety and security) Architectures</li> </ul> <p><b>Autonomous Systems B Lab</b></p> <ul style="list-style-type: none"> <li>• Based on the methods and techniques learned in Verification and Validation of Cyber-Physical Systems, Deep Learning, and Distributed Systems, a project will be completed in the field of autonomous systems.</li> <li>• Independent planning, analysis, modeling, implementation and testing of a complex application example</li> </ul> |
| <p><b>Teaching Method</b></p> | <p>Verification and Validation of Cyber-Physical Systems: Seminar (2 hpw)</p> <p>Deep Learning: Seminar (2 hpw)</p> <p>Distributed Systems: Seminar (2 hpw)</p>   |

|  |   |
|--|---|
|  | Autonomous Systems B Lab: Practical work (4 hpw)  |
| <b>Course / Teaching and Learning Methods</b>      | Seminar-style teaching, lectures, case studies, individual and group work, presentations, reflection and feedback discussions.  |
| <b>Examination Forms</b>                           | Module exam as a presentation (15 - 20 minutes, 3 times 5%) with subsequent paper (5 - 7 pages, 3 times 15%) for each seminar (seminars at all 60%) and examinations in the context of the lab: the students will independently complete a project. This includes writing a documentation of 2 to 5 pages (20%) and a final presentation of 10 to 15 minutes (20%).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <p>Verification and Validation of Cyber-Physical Systems</p> <ul style="list-style-type: none"> <li>• Smith, David: Safety Critical Systems Handbook, Elsevier Science &amp; Technology, 4th edition, 2016, ISBN-10: 0128051213.</li> <li>• Hobbs, Chris: Embedded Software Development for Safety- Critical Systems, Taylor &amp; Francis Inc , 2015, ISBN-10: 1498726704.</li> <li>• Rierson, Leanna: Developing Safety-Critical Software: A Practical Guide for Aviation Software and DO-178C Compliance, Taylor &amp; Francis Inc, 2013, ISBN-10: 143981368X.</li> <li>• Marvedel, Peter, Embedded System Design: Embedded Systems Foundations of Cyber-Physical Systems and the Internet of Things, Springer, 2017.</li> <li>• Storey, Neil: Safety Critical Computer Systems, Addison Wesley Pub Co Inc, 1st Edition, ISBN-10: 0201427877.</li> </ul> <p>Deep Learning</p> <ul style="list-style-type: none"> <li>• M. T. Hagan und H. B. Demuth: Neural Network Design. 2ndEdition. hagan.okstate.edu/NNDesign.pdf</li> <li>• C. Bishop: "Pattern Recognition and Machine Learning", Springer Verlag 2006, ISBN: 978-0-387-31073-2.</li> <li>• C. Lau: "Neural Networks: Theoretical Foundations and Analysis", IEEE Press 1992, ISBN 10: 0879422807.</li> <li>• R. Schalkoff: "Pattern Recognition: Statistical, Structural and Neural Approaches", John Wiley &amp; Sons, Inc., 1992, ISBN: 0471529745.</li> <li>• R. O. Duda, P. E. Hart, D. G. Stork: "Pattern Classification", 2<sup>nd</sup> edition, John Wiley &amp; Sons, Inc., 2000, ISBN: 978-0-471-05669- 0.</li> <li>• <a href="http://www.deeplearningbook.org">www.deeplearningbook.org</a></li> <li>• <a href="https://developer.nvidia.com/deep-learning">https://developer.nvidia.com/deep-learning</a></li> </ul> <p>Distributed Systems</p> <ul style="list-style-type: none"> <li>• George Coulouris, Jean Dollimore, Tim Kindberg, Gordon Blair: Distributed Systems: Concepts and Design, Pearson, 5th edition, 2011, ISBN-10: 0132143011.</li> </ul> |



|  |   |
|--|---|
|  | <ul style="list-style-type: none"><li>• Tanenbaum, Andrew S., Van Steen, Maarten: Distributed Systems: Principles and Paradigms, CreateSpace Independent Publishing Platform, 2nd edition, 2016, ISBN-10: 153028175X.</li><li>• Burns, Brendan: Designing Distributed Systems: Patterns and Paradigms for Scalable, Reliable Services, O'Reilly Media, 1st edition, 2017, ISBN-10: 1491983647. Industrial Communication Standards</li><li>• Zhang, Thao; Delgross, Luca: Vehicle Safety Communications: Protocols, Security, and Privacy, Wiley, 1st edition, 2012, ISBN-10: 1118132726.</li><li>• Zurawski, Richard: Industrial Communication Technology Handbook, CRC Press, 2nd edition, 2014, ISBN-10: 148220732X</li></ul> |
|--|---|

|                            |  |
|----------------------------|--|
| <b>Module Name</b>         | <b>Embedded Electronic Engineering B</b> |
| <b>Module Abbreviation</b> | ELE-B-2-7.03                             |
| <b>Module Coordinator</b>  | Prof. Dr.-Ing. Ali Hayek                 |

|                     |         |                        |     |
|---------------------|---------|------------------------|-----|
| <b>ECTS Credits</b> | 15      | <b>Total Workload</b>  | 450 |
| <b>HPW</b>          | 10      | <b>Contact Hours</b>   | 150 |
| <b>Language</b>     | English | <b>Self-Study Time</b> | 300 |

|  |   |
|--|---|
| <b>Semester of Study / Frequency of Offer / Duration</b> | 7th Semester / Summer Semester / 1 Semester |
|--|---|

|                                 |   |
|---------------------------------|---|
| <b>Qualification Objectives</b> | <p><b>Advanced Hardware Engineering</b></p> <p>The course is an advanced introduction to hardware engineering. The students learn the basic technologies and the design of complex applications in the field of hardware engineering. This includes various selected topics of VLSI design and the development of integrated circuits as well as the conception of hardware architectures for industrial applications.</p> <p>The students will gain industry-oriented competencies in the field of hardware system development at various design levels. They understand the structure, function, and design of digital integrated circuits (VLSI circuits) using modern IC design systems. They have the ability to design and model digital circuits for dependable systems (reliability, availability, maintainability and safety.) They understand the design and implementation flow for Field Programmable Gate Circuits (FPGAs) and Application Specific Integrated Circuits (ASIC), and are able to handle complex tools for the design, verification and validation of digital circuits.</p> <p><b>Internet of Things (IoT)</b></p> <p>The course is an in-depth introduction into IoT. The students will learn the basic technologies and how to design complex applications in the IoT engineering area. They will be able to understand the basic hardware and software concepts of IoT. They can develop and evaluate IoT systems. They know and can apply the IoT connectivity techniques.</p> <p><b>Product Lifecycle Management (PLM)</b></p> <p>In the course of the "Product Lifecycle Management" seminar, the students get to know the relevant processes for the product engineering and PLM for steering and control of these processes by using the connections between product / service engineering and the data generation based on socio technological development- and production processes. They will be able to identify the main processes of PLM, analyse and systemize, assess and optimize them.</p> |
|---------------------------------|---|

|                       |   |
|-----------------------|---|
|                       | <p>The students will gain application-oriented PLM competencies, i.e. methodical and analytical understanding of PLM components by getting to know and apply process-oriented collaboration and practical know how in the field of PLM. This will enable the students to work with the different PLM components and the collaboration in a job and even be able to optimize them.</p> <p><b>Embedded Electronic Engineering B Lab</b></p> <p>Students will be able to independently design an application with consideration of VLSI design concepts, IoT and by applying the knowledge of PLM.</p> <p>They will be able to present the results as part of the practical work in a scientific paper using the principles of scientific writing.</p> <p>The theoretical and practical work will give students insight into the work of a hardware developer. The practical work in small groups will strengthen the communication and coordination skills of the students.</p>   |
| <p><b>Content</b></p> | <p><b>Advanced Hardware Engineering</b></p> <ul style="list-style-type: none"> <li>• Advanced HDL constructs</li> <li>• VHDL design of complex digital circuits</li> <li>• Introduction to VLSI design (Very Large Scale Integration)</li> <li>• Dependability of hardware systems (reliability, availability, maintainability and safety).</li> <li>• Hardware architectures for industrial applications</li> <li>• FPGA design and synthesis</li> <li>• Reconfigurable systems</li> <li>• Design for testability</li> <li>• Front-end and back-end design-flow: floor planning, place and route, layout creation</li> <li>• EDA design tools using AMD Xilinx Vivado and Intel Altera as examples</li> </ul> <p><b>Internet of Things (IoT)</b></p> <p>Enabling technologies for IoT:</p> <ul style="list-style-type: none"> <li>• Application layers and protocols</li> <li>• Transmission technologies: wireless and wired</li> <li>• Architectures</li> </ul> <p>Application areas:</p> <ul style="list-style-type: none"> <li>• Consumer (smart home &amp; wearables)</li> <li>• Industrial (automation, agriculture, maritime)</li> <li>• Infrastructure (smart cities, energy management)</li> <li>• Organizational (medical &amp; healthcare, transportation, building automation)</li> </ul> <p><b>Product Lifecycle Management</b></p> <ul style="list-style-type: none"> <li>• General introduction and basic definitions. (Product Engineering Process PEP, Phases, Content)</li> <li>• Strategical PLM / PLM perspective (reasons for complexity and their impact, PDM-/PLM-strategies and paradigms, systematical product- and service engineering; CIM, CAQ).</li> <li>• Tool based PLM perspective and component view (document management, Bills of Material, versioning and Engineering Change Management (ECM), connections and dependencies between components, CAQ).</li> <li>• Operative PLM perspective (Requirements Engineering: Business-, Product-/ Service- and Data quality)</li> </ul> |

|  |   |
|--|---|
|  | <p>requirements; PLM-Tools and -implementation measures, Product-/Process- and Resource modelling).</p> <ul style="list-style-type: none"> <li>• Technical PLM-Perspective (User- / Sensor systems, application integration, IT- und Enterprise Architecture Management).</li> </ul> <p><b>Embedded Electronic Engineering B Lab</b></p> <ul style="list-style-type: none"> <li>• Based on the methods and techniques learned in seminars, a project will be completed in the field of embedded systems / product engineering.</li> <li>• Independent planning, analysis, modeling, implementation and testing of a complex application example</li> </ul>  |
| <b>Teaching Method</b>                             | <p>Advanced Hardware Engineering: Seminar (2 hpw)</p> <p>IoT: Seminar (2 hpw)</p> <p>Product Lifecycle Management: Seminar (2 hpw)</p> <p>Electronic Engineering B Lab: Practical work (4 hpw)</p>  |
| <b>Course / Teaching and Learning Methods</b>      | Seminar-style teaching, lectures, case studies, individual and group work, presentations, reflection and feedback discussions.  |
| <b>Examination Forms</b>                           | Module exam as a presentation (15 - 20 minutes, 3 times 5%) with subsequent paper (5 - 7 pages, 3 times 15%) for each seminar (seminars at all 60%) and examinations in the context of the lab: the students will independently complete a project. This includes writing a documentation of 2 to 5 pages (20%) and a final presentation of 10 to 15 minutes (20%).   |
| <b>Participation Recommendations</b>               | None.   |
| <b>Prerequisite for the award of ECTS credits</b>  | Passed final module exam.   |
| <b>Use of the module (in other study programs)</b> | -   |
| <b>Bibliography / Literature</b>                   | <p>Advanced Hardware Engineering</p> <ul style="list-style-type: none"> <li>• Lameres, B. J., Introduction to Logic Circuits &amp; Logic Design with VHDL, Springer Publisher, 2019, 2nd Edition, ISBN: 9783030124885</li> <li>• Williams. J. M., Digital VLSI Design with Verilog: A Textbook from Silicon Valley Polytechnic Institute, Springer, 2014, 2nd Edition, ISBN: 9783319047881</li> <li>• Amano, Hideharu, Principles and Structures of FPGAs, Springer, 2018, ISBN: 978-981-13-0824-6</li> <li>• Ünsalan C., Tar B., Digital System Design with FPGA: Implementation Using Verilog and VHDL, McGraw-Hill Education, 2017, ISBN: 9781259837906</li> <li>• Rowland, C.; Goodman, E.; Charlier, M.; et al., eds. (2015). Designing Connected Products: UX for the Consumer Internet of Things. O'Reilly Media. p. 726. ISBN 9781449372569.</li> <li>• Kumar A., Mangey R., The Handbook of Reliability, Maintenance, and System Safety through Mathematical Modeling, Elsevier, 1st Edition, ISBN: 9780128195826</li> </ul> |

|  |   |
|--|---|
|  | <p>Internet of Things (IoT)</p> <ul style="list-style-type: none"> <li>• Acharjya, D.P.; Geetha, M.K., eds. (2017). Internet of Things: Novel Advances and Envisioned Applications. Springer. p. 311. ISBN 9783319534725.</li> <li>• Li, S.; Xu, L.D., eds. (2017). Securing the Internet of Things. Syngress. p. 154. ISBN 9780128045053.</li> <li>• Rowland, C.; Goodman, E.; Charlier, M.; et al., eds. (2015). Designing Connected Products: UX for the Consumer Internet of Things. O'Reilly Media. p. 726. ISBN 9781449372569.</li> <li>• Thomas, Jayant; Traukina, Alena (2018). Industrial Internet Application Development: Simplify IIoT development using the elasticity of Public Cloud and Native Cloud Services. Packt Publishing. p. 25. ISBN 978-1788298599.</li> <li>• Stephenson, W. David (2018). The Future Is Smart: how your company can capitalize on the Internet of Things-- and win in a connected economy. HarperCollins Leadership. p. 250. ISBN 9780814439777.</li> </ul> <p>Product Lifecycle Management:</p> <ul style="list-style-type: none"> <li>• Eigner, Stelzer; Product Lifecycle Management: Ein Leitfaden für Product Development und Life Cycle Management, Springer, Berlin; Auflage: 2. 2009</li> <li>• Arnold, V., u. a., Product Lifecycle Management beherrschen, Springer, Berlin: 2005</li> <li>• Spur, G., Krause, F., Das virtuelle Produkt - Management der CAD - Technik, Carl Hanser, München/Wien: 1997</li> <li>• Scheer, A.-W. Wirtschaftsinformatik: Referenzmodelle für industrielle Geschäftsprozesse. 7. Aufl., Berlin [u. a.]: Springer, 1997.</li> <li>• Saaksvuori, Antti, Immonen, Anselmi: Product Lifecycle Management. 3. Aufl., Berlin [u. a.]: Springer, 2008</li> </ul> |
|--|---|